

# Adaptively Secure Laconic Function Evaluation for $\text{NC}^1$

Shweta Agrawal<sup>1</sup> and Răzvan Roşie<sup>2</sup>

<sup>1</sup> Indian Institute of Technology, Madras, India [shweta.a@cse.iitm.ac.in](mailto:shweta.a@cse.iitm.ac.in)

<sup>2</sup> University of Luxembourg, Luxembourg  
[razvan.rosie@uni.lu](mailto:razvan.rosie@uni.lu)

**Abstract.** Laconic Function Evaluation (LFE) is a novel primitive introduced by Quach, Wee and Wichs (FOCS '18) that allows two parties to perform function evaluation *laconically* in the following manner: first, Alice sends a compressed “digest” of some function – say  $\mathcal{C}$  – to Bob. Second, Bob constructs a ciphertext for his input  $M$  given the digest. Third, Alice, after receiving the ciphertext from Bob and having access to her circuit, can recover  $\mathcal{C}(M)$  and (ideally) nothing more about Bob’s message. The protocol is said to be *laconic* if the sizes of the digest and ciphertext are much smaller than the circuit size  $|\mathcal{C}|$ .

Quach, Wee and Wichs provided a construction of laconic function evaluation for general circuits under the learning with errors (LWE) assumption (with subexponential approximation factors), where all parameters grow polynomially with the depth but not the size of the circuit. Under LWE, their construction achieves the restricted notion of *selective* security where Bob’s input  $M$  must be chosen non-adaptively before even the CRS is known.

In this work, we provide the first construction of LFE for  $\text{NC}^1$ , which satisfies *adaptive* security from the ring learning with errors assumption (with polynomial approximation factors). The construction is based on the functional encryption scheme by Agrawal and Rosen (TCC 2017). Using the compiler from Quach, Wee and Wichs which bootstraps LFE to succinct, single key FE generically, we obtain a significant *simplification* of the succinct single key FE of Agrawal and Rosen.

**Keywords:** functional encryption, laconic function evaluation, laconic oblivious transfer.

## 1 Introduction

Laconic Function Evaluation (LFE) is a novel primitive that was introduced in a recent work by Quach, Wee and Wichs [11]. Intuitively, the notion proposes a setting where two parties want to evaluate a function  $\mathcal{C}$  in the following manner: Alice computes a “digest” of the circuit representation of  $\mathcal{C}$  and sends this digest to Bob; Bob computes a ciphertext CT for his input  $M$  using the received digest and sends this back to Alice; finally Alice is able to compute the value  $\mathcal{C}(M)$  in the clear without learning anything else about Bob’s input.

The work of [11] provided an elegant construction of laconic FE for all circuits from LWE. In their construction, the size of the digest, the complexity of the encryption algorithm and the size of the ciphertext only scale with the depth but not the size of the circuit. However, under LWE, their construction only achieves the restricted notion of *selective* security where Bob’s input  $M$  must be chosen non-adaptively before even the CRS is known. Moreover they must rely on LWE with subexponential modulus to noise ratio, which implies that the underlying lattice problem must be hard for subexponential approximation factors. Achieving adaptive security from LWE was left as an explicit open problem in their work.

In this work, we provide a new construction of LFE for  $\text{NC}^1$  circuits that achieves adaptive security. Our construction relies on the ring learning with errors (RLWE) assumption with polynomial modulus to noise ratio. We start with the construction by Agrawal and Rosen for functional encryption for  $\text{NC}^1$  circuits [4] and *simplify* it to achieve LFE. To achieve a laconic digest, we make use of the *laconic* oblivious transfer (LOT) primitive [7,12]. Our main result may be summarized as follows.

**Theorem 1 (LFE for  $\text{NC}^1$  – Informal).** *Assuming the hardness of RLWE with polynomial approximation factors, there exists an adaptive-secure LFE protocol for  $\text{NC}^1$ .*

We also reduce the size of the digests using laconic oblivious transfer as in [11].

The authors of [11] also studied the relations between LFE and other primitives, and in particular showed that LFE implies succinct functional encryption (FE) [5, 10]. Functional encryption is a generalisation of public key encryption in which the secret key corresponds to a function, say  $f$ , rather than a user. The ciphertext corresponds to an input  $\mathbf{x}$  from the domain of  $f$ . Decryption allows to recover  $f(\mathbf{x})$  and nothing else. Succinctness means that the size of the ciphertext in the scheme depends only on the depth of the supported circuit rather than on its size [2, 4, 8]. We may apply the LFE to FE compiler of [11] to our new, adaptively secure LFE for  $\text{NC}^1$ , thus obtaining a much *simpler* construction of adaptively secure, succinct FE for  $\text{NC}^1$ . We believe our construction of succinct FE significantly *simplifies* the original construction of Agrawal and Rosen [4] which is quite complex.

**Technical Overview.** The techniques that we use exploit the algebraic structure in the construction by Agrawal and Rosen [4]. Say that the plaintext  $M \in \{0, 1\}^k$ . If the encryption algorithm obtains a ciphertext  $\text{CT} := \{C_1, \dots, C_k\}$  where each  $C_i$  encrypts a single bit  $M_i$  of plaintext, in isolation from all different  $M_j$ , we say that the ciphertext is decomposable.

The construction supports circuits of depth  $d$  and uses a tower of moduli  $p_0 < p_1 < \dots < p_d$ . Building upon a particular levelled fully homomorphic encryption scheme (FHE) [6], it encrypts each bit of the plaintext, independently, as follows:

- first multiplication level – the ciphertext consists of a “Regev encoding”:

$$C^1 \leftarrow a \cdot s + p_0 \cdot e + M_i \in \mathcal{R}_{p_1}$$

where  $M_i \in \mathcal{R}_{p_0}$  and  $s \leftarrow_s \mathcal{R}_{p_1}$  is a fixed secret term;  $a \leftarrow_s \mathcal{R}_{p_1}$  is provided through public parameters, while  $e \leftarrow_x \mathcal{R}_{p_1}$  is the noise;

- next multiplication level – two ciphertexts are provided under the same  $s$ :

$$a' \cdot s + p_1 \cdot e' + C^1 \in \mathcal{R}_{p_2} \quad \text{and} \quad a'' \cdot s + p_1 \cdot e'' + (C^1 \cdot s) \in \mathcal{R}_{p_2}.$$

The computational pattern is repeated recursively up to  $d$  multiplication levels, and then for every bit of the input.

- an addition layer is interleaved between any two multiplication layers  $C^i$  and  $C^{i+1}$ : essentially it “replicates” ciphertexts in  $C^i$  and uses its modulus  $p_i$ .
- the public key (the “ $\vec{a}$ ”s) corresponding to the last layer are also the master public key  $\text{mpk}$  of a linear functional encryption scheme Lin-FE [1, 3].
- the decryption algorithm computes  $\mathcal{C}$  obliviously over the ciphertext, layer by layer, finally obtaining:

$$\text{CT}_{\mathcal{C}(M)} \leftarrow \mathcal{C}(M) + \text{Noise} + \text{PK}_{\mathcal{C}} \cdot s \quad (1)$$

where  $\text{PK}_{\mathcal{C}}$  is a “functional public key” that depends only on the public key and the function  $\mathcal{C}$ , and can be viewed as a succinct representation of  $\mathcal{C}$ . The term  $\text{PK}_{\mathcal{C}} \cdot s$  occurring in (1) will be removed using the functional key  $\text{sk}_{\mathcal{C}}$ , in order to recover  $\mathcal{C}(M)$  plus noise (which can be modded out).

The crux idea when building an LFE is to set the ciphertext to be essentially the data-dependent part of the FE ciphertext, while the digest to be  $\text{PK}_{\mathcal{C}}$ . Concretely, the  $\text{mpk}$  will form the  $\text{crs}$ . Whenever a circuit  $\mathcal{C}$  is to be compressed, a circuit-dependent public value – denoted  $\text{PK}_{\mathcal{C}}$  – is returned as *digest*. The receiver samples its own secret  $s$  and computes recursively the Regev encodings (seen above) as well as  $\text{PK}_{\mathcal{C}} \cdot s + p_{d-1} \cdot \text{Noise}$  (the  $\text{sk}_{\mathcal{C}}$ -dependent term), which suffice to recover  $\mathcal{C}(M)$  from (1) on the sender’s side.

Our construction departs significantly from the approach in [11]. In short, the original work makes use of generic transforms for obtaining attribute-based LFEs (AB-LFEs), obtaining AB-LFEs supporting multi-bit outputs, using the transform of [8] to hide the “attribute” in AB-LFE and compressing the digests via laconic oblivious transfer [7]. On the other hand, we do not need to go via AB-LFE, making our transformation simpler. Moreover, by relying on the adaptive security of the underlying FE scheme that we use, we obtain adaptive security.

*Towards Short Digests.* As in [11], we use the *laconic oblivious transfer* protocol in the following way: after getting the digest in the form of  $\text{PK}_{\mathcal{C}}$ , we apply a second compression round, which yields:

$$(\text{digest}_{\text{LOT}}, \hat{\mathbf{D}}) \leftarrow \text{LOT.Compress}(\text{crs}_{\text{LOT}}, \text{PK}_{\mathcal{C}}) .$$

We stress that both compression methods used are deterministic. Put differently, at any point in time, the sender, in full knowledge of her circuit representation, can recreate the digest. On the receiver’s side, instead of following the technique proposed in [11] – garbling an entire FHE decryption circuit and encrypting under an ABE the homomorphic ciphertext and the labels – we garble only the circuits that provides

$$\text{PK}_{\mathcal{C}} \cdot s + p_{d-1} \cdot \text{Noise} ,$$

while leaving the actual ciphertext intact. The advantage of such an approach resides into its conceptual simplicity, as the size of the *core* ciphertext Bob sends back to Alice remains manageable for simple  $\mathcal{C}$ , and is not suppressed by the size of the garbling scheme.

**Paper Organization.** In Section 2, we introduce the standard notations to be adopted throughout the paper, followed by the definitions of the primitives that we use as building blocks. Section 3 reviews the decomposable FE scheme proposed by Agrawal and Rosen in [4]. In Section 4, we introduce a new LFE scheme for  $\text{NC}^1$  circuits, and show in Appendix A how to combine it with laconic oblivious transfer in order to achieve a scheme with laconic digests. Appendix B recalls the LFE to single-key succinct FE compiler.

## 2 Preliminaries

**Notations.** We denote the security parameter by  $\lambda \in \mathbb{N}^*$  and we assume it is implicitly given to all algorithms in the unary representation  $1^\lambda$ . An algorithm is equivalent to a Turing machine. Algorithms are assumed to be randomized unless stated otherwise; PPT stands for “probabilistic polynomial-time” in the security parameter (rather than the total length of its inputs). Given a randomized algorithm  $\mathcal{A}$  we write the action of running  $\mathcal{A}$  on input(s)  $(1^\lambda, x_1, \dots)$  with uniform random coins  $r$  and assigning the output(s) to  $(y_1, \dots)$  by  $(y_1, \dots) \leftarrow_{\$} \mathcal{A}(1^\lambda, x_1, \dots; r)$ . When  $\mathcal{A}$  is given oracle access to some procedure  $\mathcal{O}$ , we write  $\mathcal{A}^{\mathcal{O}}$ . For a finite set  $S$ , we denote its cardinality by  $|S|$  and the action of sampling an element  $x$  uniformly at random from  $X$  by  $x \leftarrow_{\$} X$ . We let bold variables such as  $\mathbf{w}$  represent column vectors. Similarly, bold capitals stand for matrices (e.g.  $\mathbf{A}$ ). A subscript  $\mathbf{A}_{i,j}$  indicates an entry in the matrix. We overload notation for the power function and write  $\alpha^u$  to denote that variable  $\alpha$  is associated to some level  $u$  in a leveled construction. For any variable  $k \in \mathbb{N}^*$ , we define  $[k] := \{1, \dots, k\}$ . A real-valued function  $\text{NEGL}(\lambda)$  is negligible if  $\text{NEGL}(\lambda) \in \mathcal{O}(\lambda^{-\omega(1)})$ . We denote the set of all negligible functions by  $\text{NEGL}$ . Throughout the paper  $\perp$  stands for a special error symbol. We use  $\parallel$  to denote concatenation. For completeness, we recall standard algorithmic and cryptographic primitives to be used.

### 2.1 Computational Hardness Assumptions

The Learning With Error (LWE) search problem [13] asks to find the secret vector  $\mathbf{s}$  over  $\mathbb{F}_q^\ell$  given a polynomial-size set  $(\mathbf{A}, \mathbf{A} \cdot \mathbf{s} + \mathbf{e})$ , where  $\mathbf{A}$  denotes a

randomly sampled matrix over  $\mathbb{F}_q^{k \times \ell}$ , while  $\mathbf{e} \in \mathbb{F}^k$  is a small error term sampled from an appropriate distribution  $\chi$ . Roughly, the decision version of the problem, asks to distinguish between the aforementioned distribution as opposed to the uniform one.

**Definition 1 (Learning With Errors).** *The advantage of any PPT adversary  $\text{Adv}$  in distinguishing between the following two distributions is negligible:*

$$\text{Adv}_{\mathcal{A}}^{\text{LWE}}(\lambda) := \Pr[1 \leftarrow \text{Adv}(1^\lambda, \mathbf{A}, \mathbf{u})] - \Pr[1 \leftarrow \text{Adv}(1^\lambda, \mathbf{A}, \mathbf{A} \cdot \mathbf{s} + \mathbf{e})] \in \text{NEGL}(\lambda)$$

where  $\mathbf{s} \leftarrow_{\mathcal{S}} \mathbb{Z}_q^\ell$ ,  $\mathbf{A} \leftarrow_{\mathcal{S}} \mathbb{Z}_q^{k \times \ell}$ ,  $\mathbf{e} \leftarrow_{\chi} \mathbb{Z}_q^k$  and  $\mathbf{u} \leftarrow_{\mathcal{S}} \mathbb{Z}_q^k$ .

Later, Lyubashevsky, Peikert and Regev [9] proposed a ring version. Let  $\mathcal{R} := \mathbb{Z}[X]/(X^n + 1)$  for  $n$  a power of 2, while  $\mathcal{R}_q := \mathcal{R}/q\mathcal{R}$  for a safe prime  $q$  satisfying  $q \equiv 1 \pmod{2n}$ .

**Definition 2 (Ring LWE).** *For  $s \leftarrow_{\mathcal{S}} \mathcal{R}_q$ , given a polynomial number of samples that are either: (1) all of the form  $(a, a \cdot s + e)$  for some  $a \leftarrow_{\mathcal{S}} \mathcal{R}_q$  and  $e \leftarrow_{\chi} \mathcal{R}_q$  or (2) all uniformly sampled over  $\mathcal{R}_q^2$ ; the (decision)  $\text{RLWE}_{q, \phi, \chi}$  states that a PPT-bounded adversary can distinguish between the two settings only with negligible advantage.*

## 2.2 Standard Primitives

**Garbling Schemes.** Garbled circuits were introduced by Yao in 1982 [14, 15] to solve the famous ‘‘Millionaires’ Problem’’. Since then, garbled circuits became a standard building-block for many cryptographic primitives. Their definition follows.

**Definition 3 (Garbling Scheme).** *Let  $\{\mathcal{C}_k\}_\lambda$  be a family of circuits taking as input  $k$  bits. A garbling scheme is a tuple of PPT algorithms  $(\text{Garble}, \text{Enc}, \text{Eval})$  such that:*

- $(\Gamma, \text{sk}) \leftarrow_{\mathcal{S}} \text{Garble}(1^\lambda, \mathcal{C})$ : takes as input the unary representation of the security parameter and a circuit  $\mathcal{C} \in \{\mathcal{C}_k\}_\lambda$  and outputs a garbled circuit  $\Gamma$  and a secret key  $\text{sk}$ .
- $c \leftarrow_{\mathcal{S}} \text{Enc}(\text{sk}, x)$ : given as input  $x \in \{0, 1\}^k$  and the secret key  $\text{sk}$ , the encryption procedure returns an encoding  $c$ .
- $\mathcal{C}(x) \leftarrow \text{Eval}(\Gamma, c)$ : the evaluation procedure receives as inputs a garbled circuit as well as an encoding of  $x$  returning  $\mathcal{C}(x)$ .

We say that a garbling scheme  $\Gamma$  is **correct** if for all  $\mathcal{C} : \{0, 1\}^k \rightarrow \{0, 1\}^\ell$  and for all  $x \in \{0, 1\}^k$  we have that:

$$\Pr \left[ \mathcal{C}(x) = y \mid \begin{array}{l} (\Gamma, \text{sk}) \leftarrow_{\mathcal{S}} \text{GS.Garble}(1^\lambda, \mathcal{C}) \wedge \\ y \leftarrow \text{GS.Eval}(\Gamma, \text{GS.Enc}(\text{sk}, x)) \end{array} \right] = 1 .$$

**Yao’s Garbled Circuit [14].** An interesting type of garbled schemes is represented by the original proposal of Yao, which considers a family of circuits of  $k$  input wires and outputting a single bit. In his proposal, a circuit’s secret key can be viewed as two labels  $(L_i^0, L_i^1)$  for each input wire, where  $i \in [k]$ . The evaluation of the circuit at point  $x$  corresponds to an evaluation of  $\text{Eval}(\Gamma, (L_1^{x_1}, \dots, L_k^{x_k}))$ , where  $x_i$  is the  $i^{\text{th}}$  bit of  $x$  — thus the encoding  $c = (L_1^{x_1}, \dots, L_k^{x_k})$ .

## Laconic Oblivious Transfer

**Definition 4 (LOT).** A Laconic Oblivious Transfer (LOT) scheme consists of a tuple of PPT algorithms  $(\text{crsGen}, \text{Compress}, \text{Enc}, \text{Dec})$  with the following functionality:

- $\text{crs} \leftarrow \text{crsGen}(1^\lambda, 1^k, 1^d)$ : takes as input the security parameter  $\lambda$  and outputs a common reference string  $\text{crs}$ .
- $(\text{digest}, \hat{\mathbf{D}}) \leftarrow \text{Compress}(\text{crs}, \mathbf{D})$ : given a database  $\mathbf{D}$  and the  $\text{crs}$ , outputs a digest of the circuit as well as a state of the database, denoted  $\text{digest}$  and  $\hat{\mathbf{D}}$  respectively.
- $\text{CT} \leftarrow \text{Enc}(\text{crs}, \text{digest}, \ell, M_0, M_1)$ : the randomized encryption algorithm takes as input the common reference string  $\text{crs}$ , the digest, an index position  $\ell$ , and two messages; it returns a ciphertext  $\text{CT}$ .
- $M \leftarrow \text{Dec}(\text{crs}, \text{digest}, \hat{\mathbf{D}}, \text{CT}, \ell)$ : the decryption algorithm takes as input  $\hat{\mathbf{D}}$ , an index location  $\ell$ , the digest  $\text{digest}$  and the common reference string  $\text{crs}$ ; it outputs a message  $M$ .

We require an LOT to satisfy the following properties:

- **Correctness:** for all  $(M_0, M_1) \in \mathcal{M} \times \mathcal{M}$ , for all  $\mathbf{D} \in \{0, 1\}^k$  and for any index  $\ell \in [k]$  we have:

$$\Pr \left[ M = M_{\mathbf{D}[\ell]} \mid \begin{array}{l} \text{crs} \leftarrow \text{LOT.crsGen}(1^\lambda, 1^k, 1^d) \wedge \\ (\text{digest}, \hat{\mathbf{D}}) \leftarrow \text{LOT.Compress}(\text{crs}, \mathbf{D}) \wedge \\ \text{CT} \leftarrow \text{LOT.Enc}(\text{crs}, \text{digest}, \ell, M_0, M_1) \wedge \\ M \leftarrow \text{LOT.Dec}(\text{crs}, \text{digest}, \hat{\mathbf{D}}, \text{CT}, \ell) \end{array} \right] = 1 .$$

- **Laconic Digest:** the length of the digest is a fixed polynomial in the security parameter  $\lambda$ , independent of the size of the database  $\mathbf{D}$ .
- **Sender Privacy against Semi-Honest Adversaries:** there exists a PPT simulator  $\mathcal{S}$  such that for a correctly generated  $\text{crs}$  the following two distributions are computationally indistinguishable:

$$\left| \Pr [\mathcal{A}(\text{crs}, \text{LOT.Enc}(\text{crs}, \text{digest}, \ell, M_0, M_1))] - \Pr [\mathcal{A}(\text{crs}, \mathcal{S}(\text{crs}, \mathbf{D}, \ell, M_{\mathbf{D}[\ell]}))] \right| \in \text{NEGL}(\lambda) .$$

Appendix C.1 describes the LOT construction put forth in [7].

## 2.3 Functional Encryption Scheme - Public-Key Setting

We define functional encryption [5] in the public key-settings and provide a simulation-based security definition. Roughly speaking, the semantic security of the functional encryption scheme guarantees the adversary cannot learn more on  $M$  as by knowing only  $\mathcal{C}(M)$ .

**Definition 5 (Functional Encryption - Public Key Setting).** Let  $\mathcal{F} = \{\mathcal{F}_\lambda\}_{\lambda \in N}$  be an ensemble, where  $\mathcal{F}_\lambda$  is a finite collections of functions  $\mathcal{C} : \mathcal{M}_\lambda \rightarrow Y_\lambda$ . A functional encryption scheme FE in the public-key setting consists of a tuple of PPT algorithms (Setup, KeyGen, Enc, Dec) such that:

- $(\text{msk}, \text{mpk}) \leftarrow_{\$} \text{FE.Setup}(1^\lambda)$ : takes as input the unary representation of the security parameter  $\lambda$  and outputs a pair of master secret/public keys.
- $\text{sk}_{\mathcal{C}} \leftarrow_{\$} \text{FE.KeyGen}(\text{msk}, \mathcal{C})$ : given the master secret key and a function  $\mathcal{C}$ , the (randomized) key-generation procedure outputs a corresponding  $\text{sk}_{\mathcal{C}}$ .
- $\text{CT} \leftarrow_{\$} \text{FE.Enc}(\text{mpk}, M)$ : the randomized encryption procedure encrypts the plaintext  $M$  with respect to  $\text{mpk}$ .
- $\text{FE.Dec}(\text{CT}, \text{sk}_{\mathcal{C}})$ : decrypts the ciphertext  $\text{CT}$  using the functional key  $\text{sk}_{\mathcal{C}}$  in order to learn a valid message  $\mathcal{C}(M)$  or a special symbol  $\perp$ , in case the decryption procedure fails.

We say that FE satisfies correctness if for all  $\mathcal{C} : \mathcal{M}_\lambda \rightarrow Y_\lambda$  we have that:

$$\Pr \left[ y = \mathcal{C}(M) \mid \begin{array}{l} (\text{msk}, \text{mpk}) \leftarrow_{\$} \text{FE.Setup}(1^\lambda) \wedge \\ \text{sk}_{\mathcal{C}} \leftarrow_{\$} \text{FE.KeyGen}(\text{msk}, \mathcal{C}) \wedge \\ \text{CT} \leftarrow_{\$} \text{FE.Enc}(\text{mpk}, M) \wedge \\ y \leftarrow \text{FE.Dec}(\text{CT}, \text{sk}_{\mathcal{C}}) \end{array} \right] = 1 .$$

A public-key functional encryption scheme FE is semantically secure if there exists a stateful PPT simulator  $\mathcal{S}$  such that for any PPT adversary  $\mathcal{A}$ ,

$$\text{Adv}_{\mathcal{A}, \text{FE}}^{\text{FULL-SIM-FE}}(\lambda) := \left| \Pr[\text{FULL-SIM-FE}_{\text{FE}}^{\mathcal{A}}(\lambda) = 1] - \frac{1}{2} \right|$$

is negligible, where the FULL-SIM-FE experiment is described in Figure 1 (left).

<p><u>FULL-SIM-FE<sub>FE</sub><sup>A</sup>(λ):</u>  <math>b \leftarrow_{\\$} \{0, 1\}</math>  <math>(\text{msk}, \text{mpk}) \leftarrow_{\\$} \text{FE.Setup}(1^\lambda)</math>  <math>\mathcal{C} \leftarrow \mathcal{A}(\text{mpk})</math>  <math>\text{sk}_{\mathcal{C}} \leftarrow \text{FE.KeyGen}(\text{msk}, \mathcal{C})</math>  <math>M^* \leftarrow \mathcal{A}(\text{mpk}, \text{sk}_{\mathcal{C}})</math>            if <math>b = 0</math>:  <math>\text{CT}^* \leftarrow_{\\$} \text{FE.Enc}(\text{mpk}, M^*)</math>            else:  <math>\text{CT}^* \leftarrow \mathcal{S}(\text{mpk}, \text{sk}_{\mathcal{C}}, \mathcal{C}, \mathcal{C}(M^*))</math>  <math>b' \leftarrow_{\\$} \mathcal{A}(\text{CT}^*)</math>            return <math>b' = b</math></p>	<p><u>FULL-SIM-LFE<sub>LFE</sub><sup>A</sup>(λ):</u>  <math>b \leftarrow_{\\$} \{0, 1\}</math>  <math>(1^k, 1^d) \leftarrow_{\\$} \mathcal{A}(1^\lambda)</math>  <math>\text{crs} \leftarrow_{\\$} \text{LFE.crsGen}(1^\lambda, 1^k, 1^d)</math>  <math>(M^*, \mathcal{C}) \leftarrow_{\\$} \mathcal{A}(\text{crs})</math>  <math>\text{digest}_{\mathcal{C}} \leftarrow_{\\$} \text{LFE.Compress}(\text{crs}, \mathcal{C})</math>            if <math>b = 0</math>:  <math>\text{CT}^* \leftarrow_{\\$} \text{LFE.Enc}(\text{crs}, \text{digest}_{\mathcal{C}}, M^*)</math>            else:  <math>\text{CT}^* \leftarrow_{\\$} \mathcal{S}(\text{crs}, \mathcal{C}, \text{digest}_{\mathcal{C}}, \mathcal{C}(M^*))</math>  <math>b' \leftarrow_{\\$} \mathcal{A}(\text{CT}^*)</math>            return <math>b = b'</math></p>
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**Fig. 1.** Left: FULL-SIM-FE-security defined for a functional encryption scheme in the public-key setting. Right: the simulation security experiment FULL-SIM-LFE defined for a laconic function evaluation scheme LFE.

## 2.4 Laconic Function Evaluation

We described the motivation behind LFE in Section 1. We proceed with its definition from [11].

**Definition 6 (Laconic Function Evaluation [11]).** A laconic function evaluation scheme LFE for a class of circuits  $\mathcal{C}_\lambda$  consists of four algorithms (crsGen, Compress, Enc, Dec):

- $\text{crs} \leftarrow \text{LFE.crsGen}(1^\lambda, 1^k, 1^d)$ : assuming the input size and the depth of the circuit in the given class are  $k$  and  $d$ , a common reference string  $\text{crs}$  of appropriate length is generated. We assume that  $\text{crs}$  is implicitly given to all algorithms.
- $\text{digest}_{\mathcal{C}} \leftarrow \text{LFE.Compress}(\text{crs}, \mathcal{C})$ : the compression algorithm takes a description of the circuit  $\mathcal{C}$  and produces a digest  $\text{digest}_{\mathcal{C}}$ .
- $\text{CT} \leftarrow \text{LFE.Enc}(\text{crs}, \text{digest}_{\mathcal{C}}, M)$ : takes as input the message  $M$  as well as the digest of  $\mathcal{C}$  and produces a ciphertext  $\text{CT}$ .
- $\text{LFE.Dec}(\text{crs}, \text{CT}, \mathcal{C})$ : if the parameters are correctly generated, the decryption procedure recovers  $\mathcal{C}(M)$ , given the ciphertext encrypting  $M$  and circuit  $\mathcal{C}$  or a special symbol  $\perp$ , in case the decryption procedure fails.

We require the LFE scheme to achieve the following properties:

- **Correctness** - for all  $\mathcal{C} : \{0, 1\}^k \rightarrow \{0, 1\}^\ell$  of depth  $d$  and for all  $M \in \{0, 1\}^k$  we have:

$$\Pr \left[ y = \mathcal{C}(M) \mid \begin{array}{l} \text{crs} \leftarrow \text{LFE.crsGen}(1^\lambda, 1^k, 1^d) \wedge \\ \text{digest}_{\mathcal{C}} \leftarrow \text{LFE.Compress}(\text{crs}, \mathcal{C}) \wedge \\ \text{CT} \leftarrow \text{LFE.Enc}(\text{crs}, \text{digest}_{\mathcal{C}}, M) \wedge \\ y \leftarrow \text{LFE.Dec}(\text{crs}, \mathcal{C}, \text{CT}) \end{array} \right] = 1 .$$

- **Security**: there exists a PPT simulator  $\mathcal{S}$  such that for any stateful PPT adversary  $\mathcal{A}$  we have:

$$\text{Adv}_{\mathcal{A}, \text{LFE}}^{\text{FULL-SIM-LFE}}(\lambda) := \left| \Pr[\text{FULL-SIM-LFE}_{\text{LFE}}^{\mathcal{A}}(\lambda) = 1] - \frac{1}{2} \right|$$

is negligible, where FULL-SIM-LFE is defined in Figure 1 (right side).

- **Laconic outputs**: As per [11, p13-14], we require the size of digest to be laconic  $|\text{digest}_{\mathcal{C}}| \in O(\text{poly}(\lambda))$  and we impose succinctness constraints for the sizes of the ciphertext and public parameters.

## 3 Background on the AR17 FE Scheme

**Overview.** In this section, we recall a construction of functional encryption for circuits with logarithmic depth  $d$  in input length [4].



### 3.1 The AR17 Construction for NC<sup>1</sup>

In [4], the authors provided a bounded key functional encryption scheme supporting general circuits. The first distinctive feature is represented by the supported class of functions, which are now described by arithmetic, rather than Boolean circuits. Second, the ciphertexts' size in their construction is succinct, as it grows with the depth of the circuit, rather than its size. Third, the ciphertext enjoys decomposability: assuming a plaintext is represented as a vector, each of its  $k$  elements gets encrypted independently. We describe the scheme for NC<sup>1</sup>, and formalize it in Appendix C.2.

**Regev Encodings.** We commence by recalling a simple symmetric encryption scheme due to Brakerski and Vaikuntanathan [6]. Let “ $s$ ” stand for an RLWE secret acting as a secret key, while  $a$  and  $e$  are the random mask and noise:

$$\begin{aligned} c_1 &\leftarrow a && \in \mathcal{R}_p \\ c_2 &\leftarrow a \cdot s + 2 \cdot e + M && \in \mathcal{R}_p \end{aligned} \quad (2)$$

Recovering the message  $M$  (a bit, in this case) is done by subtracting  $c_1 \cdot s$  from  $c_2$  and then removing the noise through the mod 2 operator. This plain scheme comes up with powerful homomorphic properties, and is generalized in [4] to recursively support levels of encodings. Henceforth, we use the name “Regev encoding” for the following map between rings  $\mathcal{E}^i: \mathcal{R}_{p_{i-1}} \rightarrow \mathcal{R}_{p_i}$ , where:

$$\mathcal{E}^i(M) = a^i \cdot s + p_{i-1} \cdot e^i + M \in \mathcal{R}_{p_i} . \quad (3)$$

As a general notation, we write as an *superscript* of a variable the *level* to which it has been associated.

**The NC<sup>1</sup> Construction.** To be self-contained in the forthcoming parts, we give an informal specification of AR17's procedures.

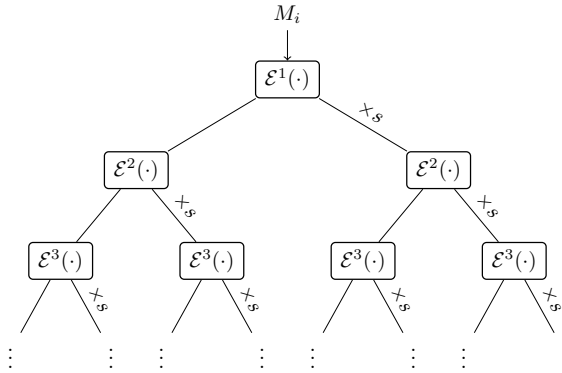
**Encryption.** The encryption procedure samples a RLWE secret  $s$ , and computes a Regev encoding for each input  $M_i \in \mathcal{R}_{p_0}$  independently. This step produces the following set  $\{\mathcal{E}^1(M_i) \mid M_i \in \mathcal{R}_{p_0} \wedge i \in [k]\}$ , where  $\mathcal{E}^1$  is the encoding mapping  $\mathcal{E}^1: \mathcal{R}_{p_0} \rightarrow \mathcal{R}_{p_1}$  defined in Equation (3). This represents the *Level 1* encoding of  $M_i$ . Next, the construction proceeds recursively; the encoding of  $M_i$  corresponding to *Level 2* takes the parent node  $P$  (in this case  $P$  is  $\mathcal{E}^1(M_i)$ ), and obtains on the left branch:

$$\mathcal{E}^2(P) = \mathcal{E}^2(\mathcal{E}^1(M_i)) = a_{1,i}^2 \cdot s + p_1 \cdot e_{1,i}^2 + ((a_{1,i}^1 \cdot s + p_0 \cdot e_{1,i}^1 + M_i) \cdot s) ,$$

while for the right branch:

$$\mathcal{E}^2(P \cdot s) = \mathcal{E}^2(\mathcal{E}^1(M_i) \cdot s) = a_{2,i}^2 \cdot s + p_1 \cdot e_{2,i}^2 + ((a_{1,i}^1 \cdot s + p_0 \cdot e_{1,i}^1 + M_i) \cdot s) ,$$

where  $(a_{1,i}^2, a_{2,i}^2) \leftarrow_s \mathcal{R}_{p_2}^2$  and noise terms are sampled from a Gaussian distribution:  $(e_{1,i}^2, e_{2,i}^2) \leftarrow_x \chi_{p_2}^2$ . This procedure is executed recursively up to a number of levels – denoted as  $d$  – as presented in Figure 2.



**Fig. 2.** The tree encoding  $M_i$  in a recursive manner corresponds to ciphertext  $\text{CT}_i$ .

Between any two successive multiplication layers, an *addition* layer is interleaved. This layer replicates the ciphertext in the previous multiplication layer (and uses its modulus). As it brings no new information, we ignore additive layers from our overview. The encoding procedure is applied for each  $M_1, \dots, M_k$ . In addition, Level 1 also contains  $\mathcal{E}^1(s)$ , while Level  $i$  (for  $2 \leq i \leq d$ ) also contains  $\mathcal{E}^i(s \cdot s)$ . Hitherto, the technique used by the scheme resembles to the ones used in levelled fully homomorphic encryption. We also remind that encodings at Level  $i$  are denoted as  $\text{CT}^i$  and are included in the ciphertext. The high level idea is to compute the function  $\mathcal{C}$  obliviously with the help of the encodings.

However, as we are in the FE setting, the ciphertext also contains additional information on  $s$ . This is achieved by using a linear functional encryption scheme Lin-FE. Namely, an extra component of the form:

$$\mathbf{d} \leftarrow \mathbf{w} \cdot s + p_{d-1} \cdot \eta \quad (4)$$

is provided as an independent part of the ciphertext, also denoted as  $\text{CT}_{\text{ind}}$ , where  $\eta \leftarrow_{\mathcal{X}} \mathcal{R}_{p_d}$  stands for a noisy term and  $\mathbf{w}$  is part of  $\text{mpk}$ .  $\mathbf{d}$  is computed once, at top level  $d$ .

The **master secret key** of the  $\text{NC}^1$  construction is set to be the  $\text{msk}$  for Lin-FE. The **master public key** consists of the  $\text{Lin-FE.mpk}$  (the vector  $\mathbf{w}$  appended to  $\mathbf{a}^d$ ) as well as of the set of vectors  $\{\mathbf{a}^1, \mathbf{a}^2, \dots, \mathbf{a}^{d-1}\}$  that will be used by each  $\mathcal{E}^i$ . Once again, we *emphasize* that the vector  $\mathbf{a}^d$  from Lin-FE.mpk coincides with the public labelling used by the mapping  $\mathcal{E}^d$ . It can be easily observed that the dimension of  $\mathbf{a}^{i+1}$  follows from a first-order recurrence:

$$|\mathbf{a}^{i+1}| = 2 \cdot |\mathbf{a}^i| + 1 \quad (5)$$

where the initial term (the length of  $\mathbf{a}^1$ ) is set to the length of the input. The extra 1, which is added per each layer is generated by the supplemental encodings of the key-dependent messages  $\{\mathcal{E}^1(s), \mathcal{E}^2(s^2), \dots, \mathcal{E}^d(s^2)\}$ .

A **functional-key**  $\text{sk}_{\mathcal{C}}$  is issued through the Lin-FE.KGen procedure in the following way: (1) using the circuit representation  $\mathcal{C}$  of the considered function as well as the public set of  $\{\mathbf{a}^1, \mathbf{a}^2, \dots, \mathbf{a}^d\}$ , a publicly computable value  $\text{PK}_{\mathcal{C}} \leftarrow \text{Eval}_{\text{PK}}(\text{mpk}, \mathcal{C})$  is obtained by performing  $\mathcal{C}$ -dependent arithmetic combinations of the values in  $\{\mathbf{a}^1, \mathbf{a}^2, \dots, \mathbf{a}^d\}$ . Then, a functional key  $\text{sk}_{\mathcal{C}}$  is issued for  $\text{PK}_{\mathcal{C}}$ . The  $\text{Eval}_{\text{PK}}(\text{mpk}, \mathcal{C})$  procedure uses  $\text{mpk}$  to compute  $\text{PK}_{\mathcal{C}}$ .

Similarly,  $\text{Eval}_{\text{CT}}(\text{mpk}, \text{CT}, \mathcal{C})$  computes the value of the function  $\mathcal{C}$  obliviously on the ciphertext. Both procedures are defined recursively; that is to compute  $\text{PK}_{\mathcal{C}}^k$  and  $\text{CT}_{\mathcal{C}(\mathbf{x})}^i$  at level  $i$ ,  $\text{PK}_{\mathcal{C}}^{i-1}$  and  $\text{CT}_{\mathcal{C}(\mathbf{x})}^{i-1}$  are needed. For a better understanding of the procedures, we will denote the encoding of  $\mathcal{C}^i(\mathbf{x})$ <sup>3</sup> by  $c^i$ , i.e.  $c^i = \mathcal{E}^i(\mathcal{C}^i(\mathbf{x}))$  and the public key or label of an encoding  $\mathcal{E}^i(\cdot)$  by  $\text{PK}(\mathcal{E}^i(\cdot))$ .

Due to space constraints, we defer the description of the evaluation algorithms to Appendix C.2.

**Decryption** works by evaluating the circuit of  $\mathcal{C}$  (known in plain by the decryptor) over the Regev encodings forming the ciphertext. At level  $d$ , the ciphertext obtained via  $\text{Eval}_{\text{CT}}$  has the following structure:

$$\text{CT}_{\mathcal{C}(\mathbf{x})} \leftarrow \text{PK}_{\mathcal{C}} \cdot s + p_{d-1} \cdot \eta^d + p_{d-2} \cdot \eta^{d-1} + \dots + p_0 \cdot \eta^1 + \mathcal{C}(\mathbf{x}) \quad (6)$$

Next, based on the independent ciphertext  $\mathbf{d} \leftarrow \mathbf{w} \cdot s + p_{d-1} \cdot \eta$  and on the functional key, the decryptor recovers

$$\text{PK}_{\mathcal{C}} \cdot s + p_{d-1} \cdot \eta' \in \mathcal{R}_{p_d}. \quad (7)$$

Finally,  $\mathcal{C}(\mathbf{x})$  is obtained by subtracting (7) from (6) and repeatedly applying the *mod* operator to eliminate the noise terms:  $(\text{mod } p_{d-1}) \dots (\text{mod } p_0)$ .

More details on  $\text{Eval}_{\text{PK}}$  and  $\text{Eval}_{\text{CT}}$  are given below.

### 3.2 Ciphertext and Public-Key Evaluation Algorithms

Let  $\mathcal{C}^n$  be the circuit representation of some function restricted to level  $n$ . For a better understanding of the procedures, we will denote the encoding of  $\mathcal{C}^n(\mathbf{x})$  by  $c^n$ , i.e.

$$c^n \leftarrow \mathcal{E}^n(\mathcal{C}^n(\mathbf{x})),$$

and the public key or label of an encoding  $\mathcal{E}^n(\cdot)$  by  $\text{PK}(\mathcal{E}^n(\cdot))$ . Furthermore,  $C^n$  are a set of level  $n$  encodings provided by the encryptor to enable the decryptor to compute  $c^n$ .

$\text{Eval}_{\text{PK}}(\cup_{t \in [n]} \text{PK}(\text{CT}^t), \ell)$  computes the label for the  $\ell^{\text{th}}$  wire in the  $n$  level circuit, from the  $i^{\text{th}}$  and  $j^{\text{th}}$  wires of  $k-1$  level:

1. Addition Level:

<sup>3</sup> Here, by  $\mathcal{C}^i$  we denote the restriction of the circuit  $\mathcal{C}$  computing  $f$  to level  $i$ .

- If  $n = 1$  (base case), then, compute  $\text{PK}(c_\ell^1) \leftarrow \text{PK}_i + \text{PK}_j$ .
- Otherwise, let  $a_i^{n-1} \leftarrow \text{Eval}_{\text{PK}}^{n-1}(\cup_{t \in [n-1]} \text{PK}(\text{CT}^t), i)$  and  $a_j^{n-1} \leftarrow \text{Eval}_{\text{PK}}^{n-1}(\cup_{t \in [n-1]} \text{PK}(\text{CT}^t), j)$ . Compute  $\text{PK}(c_\ell^n) \leftarrow a_i^{n-1} + a_j^{n-1}$ .

2. Multiplication Level:

- If  $n = 2$  (base case), then compute  $\text{PK}(c_\ell^2) \leftarrow a_i^1 \cdot a_j^1 \text{PK}(\mathcal{E}^2(s^2)) - a_j^1 \cdot \text{PK}(\mathcal{E}^2(c_i^1 s)) - a_i^1 \cdot \text{PK}(\mathcal{E}^2(c_j^1 s))$ .
- Otherwise, let  $a_i^{n-1} \leftarrow \text{Eval}_{\text{PK}}^{n-1}(\cup_{t \in [n-1]} \text{PK}(\text{CT}^t), i)$  and  $a_j^{n-1} \leftarrow \text{Eval}_{\text{PK}}^{n-1}(\cup_{t \in [n-1]} \text{PK}(\text{CT}^t), j)$ . Compute  $\text{PK}(c_\ell^n) \leftarrow a_i^{n-1} \cdot a_j^{n-1} \cdot \text{PK}(\mathcal{E}^n(s^2)) - a_j^{n-1} \cdot \text{PK}(\mathcal{E}^n(c_i^{n-1} s)) - a_i^{n-1} \cdot \text{PK}(\mathcal{E}^n(c_j^{n-1} s))$ .

$\text{Eval}_{\text{CT}}(\cup_{t \in [n]} \text{CT}^t, \ell)$  computes the encoding of the  $\ell^{\text{th}}$  wire in the  $n$  level circuit, from the  $i^{\text{th}}$  and  $j^{\text{th}}$  wires of  $n - 1$  level:

1. Addition Level:

- If  $n = 1$  (base case), then, compute  $c_\ell^1 \leftarrow \mathcal{E}^1(x_i) + \mathcal{E}^1(x_j)$ .
- Otherwise, let  $c_i^{n-1} \leftarrow \text{Eval}_{\text{CT}}^{n-1}(\cup_{t \in [n-1]} \text{CT}^t, i)$  and  $c_j^{n-1} \leftarrow \text{Eval}_{\text{CT}}^{n-1}(\cup_{t \in [n-1]} \text{CT}^t, j)$ . Compute  $\text{CT}_\ell^n \leftarrow c_i^{n-1} + c_j^{n-1}$ .

2. Multiplication Level:

- If  $n = 2$  (base case), then compute  $c_\ell^2 \leftarrow c_i^1 \cdot c_j^1 \cdot \mathcal{E}^2(s^2) - a_j^1 \cdot \mathcal{E}^2(c_i^1 s) - a_i^1 \cdot \mathcal{E}^2(c_j^1 s)$ .
- Otherwise, let  $c_i^{n-1} \leftarrow \text{Eval}_{\text{CT}}^{n-1}(\cup_{t \in [n-1]} \text{CT}^t, i)$  and  $c_j^{n-1} \leftarrow \text{Eval}_{\text{CT}}^{n-1}(\cup_{t \in [n-1]} \text{CT}^t, j)$ . Compute  $\text{CT}_\ell^n \leftarrow c_i^{n-1} \cdot c_j^{n-1} \cdot \mathcal{E}^n(s^2) - a_j^{n-1} \cdot \mathcal{E}^n(c_i^{n-1} s) - a_i^{n-1} \cdot \mathcal{E}^n(c_j^{n-1} s)$ .

## 4 Laconic Function Evaluation for $\text{NC}^1$ Circuits

We show how to instantiate an LFE protocol starting from the AR17 scheme described above (formally summarized in Appendix C.2). Furthermore, we show our construction achieves adaptive-security under RLWE with polynomial approximation factors. Finally, we compare its efficiency to the scheme for general circuits proposed in [11].

### 4.1 LFE for $\text{NC}^1$ Circuits

The core idea behind our proposal is rooted in the design of the AR17 construction. Specifically, the  $\text{mpk}$  in AR17 acts as the  $\text{crs}$  for the LFE scheme. The *compression* procedure generates a new digest by running  $\text{Eval}_{\text{PK}}$  on the

fly, given an algorithmic description of the circuit  $\mathcal{C}$  (the circuit computing the desired function). As shown in [4], the public-key evaluation algorithm can be successfully executed having knowledge of only  $\text{mpk}$  and the gate-representation of the circuit. After performing the computation, the procedure sets:

$$\text{digest}_{\mathcal{C}} \leftarrow \text{PK}_{\mathcal{C}} .$$

The digest is then handed in to the other party (say Bob).

Bob, having acquired the digest of  $\mathcal{C}$  in the form of  $\text{PK}_{\mathcal{C}}$ , encrypts his message  $M$  using the FE encryption procedure in the following way: first, a secret  $s$  is sampled from the “ $d$ -level” ring  $\mathcal{R}_{p_d}$ ;  $s$  is used to recursively encrypt each element up to level  $d$ , thus generating a tree structure, as explained in Section 3.1. Note that Bob does not need to access the ciphertext-independent part (the vector  $\mathbf{w}$  from Section 3.1) in any way. This is a noteworthy difference from the AR17 construction: an FE ciphertext is intended to be decrypted at a latter point, by (possibly) multiple functional keys. However, this constitutes an overkill when it comes to *laconic function evaluation*, as there is need to support a *single* function (for which the ciphertext is specifically created).

As a second difference from the way the ciphertext is obtained in [4], we emphasize that in our LFE protocol Bob computes directly:

$$\text{PK}_{\mathcal{C}} \cdot s + p_{d-1} \cdot \eta^d$$

where  $\text{PK}_{\mathcal{C}}$  is the digest Alice sent. Thus, there is no need to generate a genuine functional key and to obtain the ciphertext component that depends on  $\mathbf{w}$ . Directly, the two former elements constitute the ciphertext, which is sent back to Alice. Finally, the LFE *decryption* step follows immediately. Alice, after computing the *auxiliary* ciphertext in (6) “on the fly” and having knowledge of the term in (7), is able to recover  $\mathcal{C}(M)$ .

Formally, the construction can be defined as follows:

**Definition 7 (LFE for  $\text{NC}^1$  Circuits from [4]).** *Let FE denote the functional encryption scheme for  $\text{NC}^1$  circuits proposed in [4].*

- $\text{crs} \leftarrow_{\$} \text{crsGen}(1^\lambda, 1^k, 1^d)$ : *the crs is instantiated by first running FE.Setup*

$$(\text{mpk}, \text{msk}) \leftarrow_{\$} \text{FE.Setup}(1^\lambda, 1^k, 1^d) .$$

*As described, mpk has the following elements:*

$$\{\mathbf{a}^1, \dots, \mathbf{a}^{d-1}, (\mathbf{a}^d, \mathbf{w})\}$$

*with  $(\mathbf{a}^d, \mathbf{w})$  coming from the  $\text{mpk}_{\text{Lin-FE}}$  and  $\text{msk} \leftarrow \text{msk}_{\text{Lin-FE}}$ . Set  $\text{crs} \leftarrow \{\mathbf{a}^1, \dots, \mathbf{a}^d\}$  and return it.*

- $\text{digest}_{\mathcal{C}} \leftarrow \text{Compress}(\text{crs}, \mathcal{C})$ : *the compression function, given a circuit description of some function  $\mathcal{C} : \{0, 1\}^k \rightarrow \{0, 1\}$  and the crs, computes  $\text{PK}_{\mathcal{C}} \leftarrow \text{Eval}_{\text{PK}}(\mathbf{a}^1, \dots, \mathbf{a}^d, \mathcal{C})$  and then returns:*

$$\text{digest}_{\mathcal{C}} \leftarrow \text{PK}_{\mathcal{C}} .$$

- $\text{CT} \leftarrow_s \text{Enc}(\text{crs}, \text{digest}_{\mathcal{C}}, M)$ : the encryption algorithm first samples  $s \leftarrow_s \mathcal{R}_{p_d}$ , randomness  $R$  and computes recursively the Regev encodings of each bit:

$$(\text{CT}_1, \dots, \text{CT}_k, \text{CT}_{\text{ind}}) \leftarrow \text{FE.Enc}((\mathbf{a}^1, \dots, \mathbf{a}^d), (M_1, \dots, M_k); s, R)$$

Moreover, a noise  $\eta^d$  is also sampled from the appropriate distribution  $\chi$  and

$$\text{PK}_{\mathcal{C}} \cdot s + p_{d-1} \cdot \eta^d \tag{8}$$

is obtained. The ciphertext  $\text{CT}$  that is returned consists of the tuple:

$$\left( \underbrace{\text{PK}_{\mathcal{C}} \cdot s + p_{d-1} \cdot \eta^d}_{\text{CT}_a}, \underbrace{\text{CT}_1, \dots, \text{CT}_k, \text{CT}_{\text{ind}}}_{\text{CT}_b} \right) .$$

- $\text{Dec}(\text{crs}, \mathcal{C}, \text{CT})$ : first, the ciphertext evaluation is applied and  $\text{CT}_{\mathcal{C}}$  is obtained:

$$\text{CT}_{\mathcal{C}} \leftarrow \text{Eval}_{\text{CT}}(\text{mpk}, \mathcal{C}, \text{CT}) . \tag{9}$$

Then  $\mathcal{C}(M)$  is obtained via the following step:

$$(\text{CT}_{\mathcal{C}} - (\text{PK}_{\mathcal{C}} \cdot s + p_{d-1} \cdot \eta^d)) \bmod p_{d-1} \dots \bmod p_0$$

**Proposition 1 (Correctness).** *The LFE scheme in Definition 7 enjoys correctness.*

*Proof.* By the correctness of  $\text{Eval}_{\text{CT}}$ , the structure of the resulting ciphertext at level  $d$  is the following:

$$\text{PK}_{\mathcal{C}} \cdot s + p_{d-1} \cdot \eta^d + \dots + p_0 \cdot \eta^1 + \mathcal{C}(M) \tag{10}$$

Given the structure of the evaluated ciphertext in (10) as well as the second part of the ciphertext described by (8), the decryptor obtains  $\mathcal{C}(M)$  as per the decryption procedure in (9).  $\square$

**Lemma 1 (Security).** *Let FE denote the FULL-SIM-FE-secure functional encryption scheme for  $\text{NC}^1$  circuits described in [4]. The LFE scheme described in Definition 7 enjoys FULL-SIM-LFE security against any PPT adversary  $\mathcal{A}$  such that:*

$$\text{Adv}_{\mathcal{A}, \text{LFE}}^{\text{FULL-SIM-LFE}}(\lambda) \leq d \cdot \text{Adv}_{\mathcal{A}'}^{\text{RLWE}}(\lambda) .$$

*Proof (Lemma 1).* First, we describe the internal working of our simulator. Then we show how the ciphertext can be simulated via a hybrid argument, by describing the hybrid games and their code. Third, we prove the transition between each consecutive pair of hybrids.

**The Simulator.** Given the digest  $\text{digest}_{\mathcal{C}}$ , the circuit  $\mathcal{C}$ , the value of  $\mathcal{C}(M^*)$  and the  $\text{crs}$ , our simulator  $\mathcal{S}_{\text{LFE}}$  proceeds as follows:

- Samples all Regev encodings uniformly at random.

- Replaces the functional-key surrogate value  $\text{PK}_{\mathcal{C}} \cdot s + p_{d-1} \cdot \eta^d$  with  $\text{PK}_{\mathcal{C}} \cdot s + p_{d-1} \cdot \eta^d - \sum_{i=0}^{d-1} p_i \cdot \mu^{i+1} - \mathcal{C}(M^*)$ . Observe this is equivalent to running  $\text{Eval}_{\text{CT}}$  with respect to random Regev encodings, and subtracting lower noise terms and  $\mathcal{C}(M^*)$ .

**The Hybrids.** The way the simulator is built follows from a hybrid argument. Note that we only change the parts that represent the outputs of the intermediate simulators.

**Game<sub>0</sub>:** Real game, corresponding to the setting  $b = 0$  in the FULL-SIM-LFE experiment.

**Game<sub>1</sub>:** We switch from  $\text{PK}_{\mathcal{C}} \cdot s + p_{d-1} \cdot \eta^d$  to  $\text{Eval}_{\text{CT}} - \text{Noise} - \mathcal{C}(M^*)$ , such that during decryption one recovers  $\text{Noise} + \mathcal{C}(M^*)$ . The transition to the previous game is possible as we can sample the noise  $\text{Noise } \eta^*$  such that:

$$\text{SD}\left(\text{PK}_{\mathcal{C}} \cdot s + p_{d-1} \cdot \eta^d, \text{PK}_{\mathcal{C}} \cdot s + p_{d-1} \cdot \eta^d - \sum_{i=0}^{d-1} p_i \cdot \mu^{i+1} - \mathcal{C}(M^*)\right) \in \text{NEGL}(\lambda).$$

This game is identical to **Game<sub>2,0</sub>**.

**Game<sub>2,i</sub>:** We rely on the security of RLWE in order to switch all encodings on level  $(d+1) - i$  with randomly sampled elements over the corresponding rings  $\mathcal{R}_{p_{d+1-i}}$ . Note that top levels are replaced before the bottom ones; the index  $i \in [d]$ .

**Game<sub>2,d</sub>:** This setting corresponds to  $b = 1$  in the FULL-SIM-LFE experiment.

We now prove the transitions between the hybrid games.

*Claim (Distance between Game<sub>0</sub> and Game<sub>1</sub>).* There exists a PPT simulator  $\mathcal{S}_1$  such that for any stateful PPT adversary  $\mathcal{A}$  we have:

$$\text{Adv}_{\mathcal{A}}^{\text{Game}_0 \rightarrow \text{Game}_1}(\lambda) := \left| \Pr[\text{Game}_0^{\mathcal{A}}(\lambda) \Rightarrow 1] - \Pr[\text{Game}_1^{\mathcal{A}}(\lambda) \Rightarrow 1] \right|$$

is statistically close to 0.

*Proof.* Let  $\mathcal{D}_0$  (respectively  $\mathcal{D}_1$ ) be the distribution out of which  $\text{CT}_a$  is sampled in **Game<sub>0</sub>** (respectively **Game<sub>1</sub>**). The statistical distance between  $\mathcal{D}_0$  and  $\mathcal{D}_1$  is negligible:

$$\begin{aligned} \text{SD}(\mathcal{D}_0, \mathcal{D}_1) &= \frac{1}{2} \cdot \sum_{v \in \mathcal{R}_{p_d}} \left| \Pr[v = \text{PK}_{\mathcal{C}} \cdot s + p_{d-1} \cdot \eta^d] - \right. \\ &\quad \left. \Pr[v = \text{Eval}_{\text{CT}}(\mathcal{C}) + \sum_{i=0}^{d-1} p_i \cdot \mu^{i+1} - \mathcal{C}(M^*)] \right| \\ &= \frac{1}{2} \cdot \sum_{v \in \mathcal{R}_{p_d}} \left| \Pr[v = \text{PK}_{\mathcal{C}} \cdot s + p_{d-1} \cdot \eta^d] - \right. \\ &\quad \left. \Pr[v = \text{PK}_{\mathcal{C}} \cdot s + \sum_{i=0}^{d-1} p_i \cdot \mu^{i+1}] \right| \end{aligned}$$

We apply the same technique as per [4, p. 27], and we sample a noise term  $\eta_*^d$  such that:

$$\text{SD}(\eta_*^d, \sum_{i=0}^{d-1} p_i \cdot \nu^{i+1}) \leq \epsilon .$$

Thus, the advantage of any adversary in distinguishing between  $\text{Game}_0$  and  $\text{Game}_1$  is statistically close to 0.  $\square$

Games 1 and 2.0 are identical.

*Claim (Distance between  $\text{Game}_{2,i}$  and  $\text{Game}_{2,i+1}$ ).* There exists a PPT simulator  $\mathcal{S}_1$  such that for any stateful PPT adversary  $\mathcal{A}$  we have:

$$\begin{aligned} \text{Adv}_{\mathcal{A}}^{\text{Game}_{2,i} \rightarrow \text{Game}_{2,i+1}}(\lambda) &:= \left| \Pr[\text{Game}_{2,i}^{\mathcal{A}}(\lambda) \Rightarrow 1] - \Pr[\text{Game}_{2,i+1}^{\mathcal{A}}(\lambda) \Rightarrow 1] \right| \\ &\leq \text{Adv}_{\mathcal{B}}^{\text{RLWE}}(\lambda) . \end{aligned}$$

*Proof.* The reduction  $\mathcal{B}$  is given as input a sufficiently large set of elements which are either: RLWE samples of the form  $a \cdot s^{(d-i)} + p_{d-1-i} \cdot \eta^{(d-i)}$  or  $u$  where  $u \leftarrow_{\$} \mathcal{R}_{p_{d-i}}$ .

$\mathcal{B}$  constructs  $\text{CT}_b$  as follows: for upper levels  $j > d - i$ ,  $\mathcal{B}$  samples elements uniformly at random over  $\mathcal{R}_{p_j}$ . For each lower levels  $j < d - i$ ,  $\mathcal{R}$  samples independent secrets  $s^j$  and builds the lower level encodings correctly, as stated in Figure 2.

For challenge level  $d - i$ ,  $\mathcal{B}$  takes the challenge values of the RLWE experiment – say  $z$  –, and produces the level  $d - i$  encodings from the level  $d - 1 - i$  encodings as follows:

- for each encoding  $\mathcal{E}^{d-1-i}$  in level  $d - i - 1$ , produce a left encoding in level  $d - i$ :

$$z_1^{d-i} + \mathcal{E}^{d-1-i} .$$

- for each encoding  $\mathcal{E}^{d-1-i}$  in level  $d - i - 1$ , produce a right encoding in level  $d - i$ :

$$z_2^{d-i} + \mathcal{E}^{d-1-i} \cdot s^{d-1-i} .$$

Note that  $s^{d-1-i}$  is known in plain by  $\mathcal{B}$ .

Considering level 1, the message bits themselves are encrypted, as opposed to encodings from lower levels.

The first component of the ciphertext –  $\text{CT}_a$  – is computed by getting  $v \leftarrow \text{Eval}_{\text{CT}}(\mathcal{C}, \{C^i\}_{i \in [d]})$  over the encodings, and then adding the noise and the value  $\mathcal{C}(M^*)$ .

The adversary is provided with the simulated ciphertext. The analysis of the reduction is immediate: the winning probability in the case of  $\mathcal{B}$  is identical to that of the adversary distinguishing.  $\square$

Finally, we apply the union bound and conclude with:

$$\text{Adv}_{\mathcal{A}, \text{LFE}}^{\text{FULL-SIM-LFE}}(\lambda) \leq d \cdot \text{Adv}_{\mathcal{A}'}^{\text{RLWE}}(\lambda) .$$

$\square$

In Appendix A, we show how the digest can be further compressed.



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## A Achieving a Laconic Digest

In this part, we put forth an LFE scheme with a digest independent by the size of the input, starting from the LFE construction in Section 4.1. The main idea consists in using a laconic oblivious transfer (see Appendix C.1) in order to generate a small digest. In doing so, our strategy departs from the one used in [11].

The bulk of the idea is to observe that the encryption algorithm in Section 4.1 outputs a ciphertext having two components: the first one consists of the AR17 ciphertext; the second component is literally  $\text{PK}_{\mathcal{C}} \cdot s + p_{d-1} \cdot \eta^d$ . We create an auxiliary circuit  $\mathcal{C}_{aux}$  that takes as input  $\text{PK}_{\mathcal{C}}$  and outputs  $\text{PK}_{\mathcal{C}} \cdot s + p_{d-1} \cdot \eta^d$ . We garble  $\mathcal{C}_{aux}$  using Yao's garbling scheme (Section 2.2) and use LOT to encrypt the garbling labels. Thus, instead of garbling a circuit that produces the entire ciphertext (i.e. following the template proposed by [11]) we garble  $\mathcal{C}_{aux}$ . Such an approach is advantageous, as the complexity of the latter circuit, essentially performing a multiplication of two elements over a polynomial ring, is in general low when compared to a circuit that reconstructs the entire ciphertext. We present our construction below:

**Definition 8 (LFE for  $\text{NC}^1$  with Laconic Digest).** *Let LFE denote the laconic function evaluation scheme for  $\text{NC}^1$  circuits presented in Section 4.1, LOT stand for a laconic oblivious transfer protocol and GS denote Yao's garbling scheme.  $\overline{\text{LFE}}$  stands for a laconic function evaluation scheme for  $\text{NC}^1$  with digest of size  $O(\lambda)$ :*

- $\overline{\text{crs}} \leftarrow_s \overline{\text{LFE}}.\text{crsGen}(1^\lambda, 1^k, 1^d)$ : the  $\overline{\text{crs}}$  is instantiated by running the LFE.Setup

$$\text{crs} \leftarrow_s \text{LFE.Setup}(1^\lambda, 1^k, 1^d) .$$

Let  $\text{crs}_{\text{LOT}}$  stand for the common reference string of the LOT scheme.

Set  $\overline{\text{crs}} \leftarrow (\text{crs}, \text{crs}_{\text{LOT}})$  and return it.

- $\text{digest}_{\mathcal{C}} \leftarrow \overline{\text{LFE}}.\text{Compress}(\overline{\text{crs}}, \mathcal{C})$ : the compression function, given a circuit description of some function  $\mathcal{C} : \{0, 1\}^k \rightarrow \{0, 1\}$  and the  $\overline{\text{crs}} \leftarrow (\text{crs}, \text{crs}_{\text{LOT}})$ , computes  $\text{PK}_{\mathcal{C}} \leftarrow \text{LFE.Compress}(\text{crs}, \mathcal{C})$  and then returns:

$$\text{digest}_{\mathcal{C}} \leftarrow \text{PK}_{\mathcal{C}} .$$

Then, using  $\text{crs}_{\text{LOT}}$ , a new digest/database pair is obtained as:

$$(\overline{\text{digest}_{\mathcal{C}}}, \hat{\mathbf{D}}) \leftarrow \text{LOT.Compress}(\text{crs}_{\text{LOT}}, \text{digest}_{\mathcal{C}}) .$$

Finally,  $\overline{\text{digest}_{\mathcal{C}}}$  is returned.

- $\text{CT} \leftarrow \text{LFE.Enc}(\overline{\text{crs}}, \overline{\text{digest}}_{\mathcal{C}}, M)$ : after parsing the  $\overline{\text{crs}}$  as  $(\text{crs}_{\text{LOT}}, \text{crs})$ <sup>4</sup>, the encryption algorithm first samples  $s \leftarrow \mathcal{R}_{p_d}$ , randomness  $R$  and computes recursively the Regev encodings of each bit:

$$\text{CT}_{\text{LFE}} \leftarrow \text{LFE.Enc}(\text{crs}, (M_1, \dots, M_k); s, R)$$

Moreover, a noise  $\eta^{d-1}$  is also sampled from the appropriate distribution  $\chi$  and an auxiliary circuit  $\mathcal{C}_{\text{aux}}$  that returns

$$\text{PK}_{\mathcal{C}} \cdot s + p_{d-1} \cdot \eta^d \tag{11}$$

is obtained, where  $\text{PK}_{\mathcal{C}}$  constitutes the input and  $s, p_{d-1} \cdot \eta^d$  are hardwired.

Then,  $\mathcal{C}_{\text{aux}}$  is garbled by Yao's garbling scheme:

$$(\Gamma, \{L_i^0, L_i^1\}_{i=1}^t) \leftarrow \text{GS.Garble}(\mathcal{C}_{\text{aux}})$$

and the labels are encrypted under LOT:

$$\overline{L}_i \leftarrow \text{LOT.Enc}(\text{crs}_{\text{LOT}}, \overline{\text{digest}}_{\mathcal{C}}, i, L_i^0, L_i^1), \forall i \in [t]$$

The ciphertext  $\text{CT}$  is set to be the tuple  $(\text{CT}_{\text{LFE}}, \Gamma, \overline{L}_1, \dots, \overline{L}_t)$ .

- $\overline{\text{LFE}}.\text{Dec}(\overline{\text{crs}}, \mathcal{C}, \text{CT})$ : First, the labels  $L_i$  corresponding to the binary decomposition of  $\text{PK}_{\mathcal{C}}$  are obtained:

$$L_i^{\text{PK}_{\mathcal{C}}[i]} \leftarrow \text{LOT.Dec}(\text{crs}_{\text{LOT}}, \overline{\text{digest}}_{\mathcal{C}}, i, \overline{L}_i)$$

When feeding  $\Gamma$  with  $L_i$ , the decryptor recovers  $\text{PK}_{\mathcal{C}} \cdot s + p_{d-1} \cdot \eta^{d-1}$ .

Then, the ciphertext evaluation is applied and  $\text{CT}_{\mathcal{C}}$  is obtained:

$$\text{CT}_{\mathcal{C}} \leftarrow \text{Eval}_{\text{CT}}(\text{mpk}, \mathcal{C}, \text{CT}) . \tag{12}$$

Then  $\mathcal{C}(M)$  is obtained via the following step:

$$\left( \text{CT}_{\mathcal{C}} - (\text{PK}_{\mathcal{C}} \cdot s + p_{d-1} \cdot \eta^d) \right) \bmod p_{d-1} \dots \bmod p_0$$

**Proposition 2 (Correctness).** *The laconic function evaluation scheme in Definition 8 is correct.*

*Proof.* By the correctness of the LOT scheme, the correct labels corresponding to the value of  $\text{PK}_{\mathcal{C}}$  are recovered. By the correctness of the garbling scheme, when fed with the correct labels, the value of  $\text{PK}_{\mathcal{C}} \cdot s + p_{d-1} \cdot \eta^d$  is obtained. Finally, by the correctness of LFE, we recover  $\mathcal{C}(M)$ .  $\square$

<sup>4</sup> Even if we omit that explicitly, note that  $\text{crs}$  includes  $\text{mpk}$ , consistently with the execution of the subroutine  $\text{FE.Enc}()$ .

In what follows, we show the scheme achieves simulation security, assuming the same security level from the underlying primitive.

**Theorem 2 (Security).** *Let  $\mathcal{C}_\lambda$  denote a class of circuits and let  $\overline{\text{LFE}}$  denote the laconic function evaluation scheme defined Definition 8. Let GS and LOT denote the underlying garbling scheme, respectively laconic oblivious transfer scheme. The advantage of any PPT bounded adversary  $\mathcal{A}$  against the adaptive simulation security of the  $\overline{\text{LFE}}$  scheme is bounded as follows:*

$$\text{Adv}_{\overline{\text{LFE}}, \mathcal{A}}^{\text{FULL-SIM-LFE}}(\lambda) \leq \text{Adv}_{\text{GS}, \mathcal{B}_1}^{\text{FULL-SIM-GS}}(\lambda) + \text{Adv}_{\text{LOT}, \mathcal{B}_2}^{\text{FULL-SIM-LOT}}(\lambda) + \text{Adv}_{\overline{\text{LFE}}, \mathcal{B}_3}^{\text{FULL-SIM-LFE}}(\lambda).$$

*Proof (Theorem 2).* We show the construction achieves adaptive security. In our proof we make use of the LFE simulator as well as the simulation security of the garbling protocol and of the LOT scheme.

**Simulator.** We build the simulator  $\mathcal{S}_{\overline{\text{LFE}}}$  as follows: first, we run the simulator of the underlying LFE scheme in order to obtain the bulk of the ciphertext. Independently, we run the simulator of the garbled circuit  $\mathcal{S}_{\text{GS}}$ . Finally, we run the simulator of the LOT scheme ( $\mathcal{S}_{\text{LOT}}$ ) on the labels obtained from the  $\mathcal{S}_{\text{GS}}$ .

The proof requires a hybrid argument. We present below the games and the transitions between them:

**Game<sub>0</sub>:** corresponds to the FULL-SIM-LFE game where  $b$  is set to 0.

**Game<sub>1</sub>:** in this game, we use the simulator  $\mathcal{S}_{\text{LOT}}$  to simulate the corresponding component of the ciphertext (i.e.  $\overline{L}_i$ ). The distance to the previous game is bounded by the simulation security of the LOT protocol.

**Game<sub>2</sub>:** in this game, we switch to the usage of the garbled scheme simulator GS in order to compute the labels corresponding to the second part of the ciphertext. The game hop is bounded by the simulation security of the garbling scheme.

**Game<sub>3</sub>:** we change the bulk ciphertext to the one obtained by the  $\mathcal{S}_{\text{LFE}}$  simulator, the distance to the previous game being bounded by the advantage in Lemma 1.

*Claim (Transition between Game<sub>0</sub> and Game<sub>1</sub>).* The advantage of any PPT adversary in distinguishing between Game<sub>0</sub> and Game<sub>1</sub> is bounded as follows:

$$\text{Adv}_{\mathcal{A}_1}^{\text{Game}_0 \rightarrow \text{Game}_1}(\lambda) \leq \text{Adv}_{\text{LOT}, \mathcal{B}_1}^{\text{FULL-SIM-LOT}}(\lambda).$$

*Proof (Game<sub>0</sub> → Game<sub>1</sub>).* We provide a reduction to the LOT security experiment, which initially samples and publishes  $\text{crs}_{\text{LOT}}$ . Let  $\mathcal{B}_1$  denote the reduction.  $\mathcal{B}_1$  simulates the LFE game in front of the PPT bounded adversary  $\mathcal{A}_1$ : (1) First, it samples  $\text{crs}_{\text{LFE}}$ , and publishes  $(\text{crs}_{\text{LFE}}, \text{crs}_{\text{LOT}})$ ; (2) Next,  $\mathcal{B}_1$  receives from the

LFE adversary  $\mathcal{A}_1$  the tuple  $(\mathcal{C}, \mathcal{C}(M^*))$ ; (3) computes the digest  $\text{digest}_{\mathcal{C}}$ , the underlying LFE ciphertext, and the garbled circuits with the associated labels.

Next,  $\mathcal{B}_1$  impersonates an adversary against the LOT security game, by submitting the tuple  $(\text{PK}_{\mathcal{C}} \cdot s + p_{d-1} \cdot \eta^d, \{L_{i,i0}, L_{i,1}\}_{i \in |\text{digest}_{\mathcal{C}}|})$ .

The LOT game picks a random  $b \in \{0, 1\}$  and provides the adversary with either a correctly generated LOT ciphertext encrypting the labels  $L_i^0, L_i^1$  under position  $i$ , or by a simulated ciphertext. The latter ciphertext is generated by  $\mathcal{S}_{\text{LOT}}$ .

Thus  $\mathcal{B}_1$  obtains the entire  $\overline{\text{LFE}}$  ciphertext, which is passed to  $\mathcal{A}_1$ .  $\mathcal{A}_1$  returns a bit, indicating its current setting. It is clear that any PPT adversary able to distinguish between the two settings breaks the LOT security of the underlying LOT scheme.  $\square$

*Claim (Transition between Game<sub>1</sub> and Game<sub>2</sub>).* The advantage of any PPT adversary to distinguish between Game<sub>1</sub> and Game<sub>2</sub> is bounded as follows:

$$\text{Adv}_{\mathcal{A}_2}^{\text{Game}_1 \rightarrow \text{Game}_2}(\lambda) \leq \text{Adv}_{\mathcal{GS}, \mathcal{B}_2}^{\text{FULL-SIM-GS}}(\lambda).$$

*Proof (Game<sub>1</sub>  $\rightarrow$  Game<sub>2</sub>).* By the input and circuit privacy of the garbled circuit, there exists  $\mathcal{S}_{\text{GS}}$  that produces a tuple  $(\tilde{T}, \{\tilde{L}_i^0, \tilde{L}_i^1\}_{i \in [t]})$ .

Let  $\mathcal{B}_2$  denote the reduction, and let  $\mathcal{A}_2$  denote the adversary against the LFE game. As for the previous game,  $\mathcal{B}_2$  samples and publishes  $\text{crs}_{\overline{\text{LFE}}}$ ,  $\mathcal{A}_2$  provides  $(\mathcal{C}, \mathcal{C}(M^*))$ .  $\mathcal{B}_2$  builds the LFE ciphertext.

Next,  $\mathcal{B}_2$  impersonates an adversary against the GS security experiment.  $\mathcal{B}_2$  provides the GS game with  $(\mathcal{C}_{\text{aux}}, \text{PK}_{\mathcal{C}} \cdot s + p_{d-1} \cdot \eta^d)$ , and receives either correctly generated garbled circuit and labels or a simulated garbled circuit and the simulated labels.

Thus, if  $\mathcal{A}_2$  distinguishes between the two games,  $\mathcal{B}_2$  distinguishes between the two distributions of labels in the GS game.  $\square$

*Claim (Transition between Game<sub>2</sub> and Game<sub>3</sub>).* The advantage of any PPT adversary to distinguish between Game<sub>2</sub> and Game<sub>3</sub> is bounded as follows:

$$\text{Adv}_{\mathcal{A}_3}^{\text{Game}_2 \rightarrow \text{Game}_3}(\lambda) \leq \text{Adv}_{\text{LFE}, \mathcal{B}_3}^{\text{FULL-SIM-LFE}}(\lambda).$$

*Proof (Game<sub>2</sub>  $\rightarrow$  Game<sub>3</sub>).* Finally, we use the security of the LFE scheme in order to switch elements of the first component of the ciphertext to simulated ones. The advantage of any adversary noticing the transition is bounded by the advantage of winning the FULL-SIM-LFE security of the underlying LFE.

The FULL-SIM-LFE experiment generates  $\text{crs}_{\text{LFE}}$ .  $\mathcal{B}_3$  samples  $\text{crs}_{\text{LOT}}$  and provides the resulting  $\overline{\text{crs}}$  to  $\mathcal{A}_3$ . The adversary returns  $(\mathcal{C}, \mathcal{C}(M^*))$ , which are forwarded to FULL-SIM-LFE. The experiment generates the ciphertext either correctly or using  $\mathcal{S}_{\text{LFE}}$ .

Then  $\mathcal{B}_3$  employs  $\mathcal{S}_{\text{GS}}$  and  $\mathcal{S}_{\text{LOT}}$  to obtain the remaining  $\overline{\text{LFE}}$  ciphertext components and runs  $\mathcal{A}_3$  on the full ciphertext.  $\mathcal{B}_3$  returns the corresponding output to the setting indicated by  $\mathcal{A}_3$ .

We also note that this setting simulates the FULL-SIM-LFE experiment with  $b = 1$ .  $\square$

This completes the proof of Theorem 2. □

**Size of parameters.** A main benefit of the LFE schemes in Definitions 7 and 8 is the *simplicity* of the crs,  $\text{digest}_{\mathcal{C}}$  and ciphertext structures. These are essentially elements over known rings of polynomials. The sizes of the rings  $\mathcal{R}_{p_i}$  are enforced by the prime factors  $p_0, p_1, \dots, p_d$ . As stated in [4, Appendix E]  $p_d \in O(B_1^{2^d})$  where  $B_1$  denotes the magnitude of the noise used for Level-1 encodings and being bounded by  $p_1/4$  in order to ensure correct decryption<sup>5</sup>. However, we observe that a tighter bound may have the following form:  $p_d \in O(B_1^{2^{\text{Mul}}})$ , where  $\text{Mul}$  denotes the number of multiplicative levels in the circuit. From the point of view of space complexity, we rely on the original analysis of [4] that provides guidelines for the size of the primes  $p_0, p_1, \dots, p_d$ . As we are only interested in the case stipulating that  $|p_d|$  belongs to  $O(\text{poly}(\lambda))$ . The condition can be achieved by imposing further restrictions on the multiplicative depth of the circuit, namely  $d \in O(\log(\text{poly}(\lambda)))$ , meaning that the digest is short enough whenever the circuit belong to  $\text{NC}^1$  class. The size of the digest in Definition 8 is laconic, given the fact that LOT produces laconic digests.

*Handling multiple bits of output.* Regarding the ability to support multiple output bits (say  $\ell$ ), this is inherent by using an arithmetic circuit and setting  $\lfloor \log(p_0) \rfloor = \ell$ . If binary circuits are needed, then  $\ell$  public evaluation can be obtained through  $\text{Eval}_{\text{PK}}$ , and the scheme modified by having the garbling circuit producing  $\ell$  outputs.

Another potentially beneficial point of comparison is the ability of the schemes in Definitions 7 and 8 to support a bounded number of circuits without changing the data-dependent ciphertext. This happens when the second party involved is stateful: given two functions  $f, g$  represented through circuits  $\mathcal{C}_f, \mathcal{C}_g$ , and assuming the receiver stores  $s$  (it is stateful), the  $\text{Enc}$  algorithm may simply recompute  $\text{PK}_g \cdot s + p_{d-1} \cdot \mu^d$  in the same manner it has already computed  $\text{PK}_{\mathcal{C}} \cdot s + p_{d-1} \cdot \eta^d$ .

## B Bootstrapping LFE to succinct single-key FE

Quash *et al.* [11] proposed a compiler from an LFE protocol to a succinct single-key functional encryption scheme. We recall their result below.

**Theorem 3 (LFE to FE Compiler from [11]).** *Assuming the existence of a selectively (respectively adaptively) secure LFE scheme, there exists a succinct, selective (respectively adaptive), simulation-secure, single-key, functional encryption scheme.*

*If an LFE, for a circuit family  $\{\mathcal{C}^k, d\}_{k \in \mathbb{N}, d \in \mathbb{N}}$  with circuit parameters  $(k, d)$ , has an encryption circuit of size  $T = T(\lambda, k, d)$  (where the inputs to the encryption circuit are both the message and the randomness for the LFE encryption),*

<sup>5</sup> Our scheme is intended to support Boolean circuits, thus  $p_0$  is set to 2.

then the resulting FE has encryption time and ciphertext size  $T \cdot \text{poly}(\lambda)$ .

Their transformation follows:

- $\overline{\text{FE}}.\text{Setup}(1^\lambda)$ : compute  $\text{crs}_{\text{LFE}} \leftarrow \text{LFE}.\text{crsGen}(1^\lambda)$  and  $(\text{mpk}_{\text{FE}}, \text{msk}_{\text{FE}}) \leftarrow \text{FE}.\text{Setup}(1^\lambda)$ .  
Return:

$$\begin{aligned} \overline{\text{mpk}} &\leftarrow \text{mpk}_{\text{FE}} \\ \overline{\text{msk}} &\leftarrow (\text{crs}_{\text{LFE}}, \text{msk}_{\text{FE}}) \end{aligned}$$

- $\overline{\text{FE}}.\text{KeyGen}(\overline{\text{msk}}, \mathcal{C})$ : let  $\mathcal{C}_{\text{LFE}}$  be a circuit that takes as input the plaintext  $M$  and some randomness  $R$  and returns an LFE ciphertext:  $\text{LFE}.\text{Enc}(\text{crs}_{\text{LFE}}, \text{digest}_{\mathcal{C}_{\text{LFE}}}, M; R)$ .  
Issue a functional key:

$$\text{sk}_{\mathcal{C}_{\text{LFE}}} \leftarrow \text{FE}.\text{KeyGen}(\text{msk}_{\text{FE}}, \mathcal{C}_{\text{LFE}}) .$$

Return:

$$\overline{\text{sk}}_{\mathcal{C}} \leftarrow (\text{sk}_{\mathcal{C}_{\text{LFE}}}, \mathcal{C}, \text{crs}_{\text{LFE}}) .$$

- $\overline{\text{FE}}.\text{Enc}(\overline{\text{mpk}}, M)$ : sample randomness  $R$  and return

$$\overline{\text{CT}} \leftarrow \text{FE}.\text{Enc}(\text{mpk}_{\text{FE}}, (M, R)) .$$

- $\overline{\text{FE}}.\text{Dec}(\overline{\text{sk}}_{\mathcal{C}}, \overline{\text{CT}})$ : parse  $\overline{\text{sk}}_{\mathcal{C}}$  as  $(\text{sk}_{\mathcal{C}_{\text{LFE}}}, \mathcal{C}, \text{crs}_{\text{LFE}})$ , obtain  $\text{digest}_{\mathcal{C}}$  from  $\mathcal{C}$ , then return

$$\text{LFE}.\text{Dec}(\text{crs}_{\text{LFE}}, \text{digest}_{\mathcal{C}}, \text{FE}.\text{Dec}(\text{sk}_{\mathcal{C}_{\text{LFE}}}, \overline{\text{CT}})) .$$

### B.1 A New FE from the “LFE to FE” Compiler

In this part, we consider a novel, single-key and succinct FE, obtained through the transform above, using as building blocks: (1) the LFE scheme proposed in Appendix A, and (2) the FE scheme from [4]. In our analysis, we consider the size of the ciphertext. For the case of the original AR17 construction described in [4], its size grows exponentially within the multiplicative level, but also linearly with the length of the plaintext.

On the contrary, considering the  $\overline{\text{FE}}$  obtained through the compiler above, the ciphertext should support  $\mathcal{C}_{\text{LFE}}$ . Since  $\mathcal{C}_{\text{LFE}}$  outputs three main components: a) an AR17 ciphertext, b) a garbled circuit, c) a set of LOT labels, the multiplicative depth of the circuit computing these components will be lower: the depth-dominating component is the one *computing* the AR17 ciphertext. However,  $\mathcal{C}_{\text{LFE}}$  can handle computing all Regev encodings in parallel; thus, the circuit  $\mathcal{C}_{\text{LFE}}$  has a smaller depth compared to  $\mathcal{C}$ <sup>6</sup>.

We assume we bootstrap using the FE from Theorem 3. Therefore, the size of the  $\overline{\text{FE}}$  ciphertext (i.e. AR17 ciphertext) is enforced by the depth  $\mathcal{C}_{\text{LFE}}$ , which is lower compared to the depth of the original  $\mathcal{C}$ . As a consequence, the ciphertext corresponding to  $\overline{\text{FE}}$  is asymptotically more efficient.

<sup>6</sup> This is also guaranteed by the laconic requirement of an LFE, which stipulates the time of computing the ciphertext should be lower than the size of the original circuit.

## C Further Definitions

### C.1 Construction of LOT in [7]

In [7], Cho *et al.* first present an LOT construction based on the DDH assumption, for which the hash function compresses a database of length  $2\lambda$  into a digest of length  $\lambda$  (factor-2 compression). Afterwards, the authors explain how to build an LOT scheme for a database of arbitrarily  $\text{poly}(\lambda)$  length  $M$ .

For the factor-2 compression LOT, the construction is based on two primitives called *Somewhere Statistically Binding Hash Functions* (SSB) and *Hash Proof System* (HPS). We recall their definitions.

**Definition 9 (Somewhere Statistically Binding Hash).** *An SSB hash function SSBH consists of three algorithms  $\text{crsGen}$ ,  $\text{bindingCrsGen}$  and  $\text{Hash}$  with the following syntax.*

- $\text{crs} \leftarrow \text{crsGen}(1^\lambda)$ : takes the security parameter  $\lambda$  as input and outputs a common reference string  $\text{crs}$ .
- $\text{crs} \leftarrow \text{bindingCrsGen}(1^\lambda, i)$ : takes as input the security parameter  $\lambda$  and an index  $i \in [2\lambda]$ , and outputs a binding common reference string  $\text{crs}$ .
- $y \leftarrow \text{Hash}(\text{crs}, x)$ . For some domain  $\mathcal{D}$ , it takes as input a common reference string  $\text{crs}$  and a string  $x \in \mathcal{D}^{2\lambda}$ , and outputs a string  $y \in \{0, 1\}^\lambda$ .

We require the following properties from an SSB hash function:

1. **Statistically Binding at Position  $i$ :** For every  $i \in [2\lambda]$  and an overwhelming fraction of  $\text{crs}$  in the support of  $\text{bindingCrsGen}(1^\lambda, i)$  and every  $x \in \mathcal{D}^{2\lambda}$ , we have that  $(\text{crs}, \text{Hash}(\text{crs}, x))$  uniquely determines  $x_i$ . More formally, for all  $x' \in \mathcal{D}^{2\lambda}$  such that  $x_i \neq x'_i$  we have that  $\text{Hash}(\text{crs}, x') \neq \text{Hash}(\text{crs}, x)$ .
2. **Index Hiding:** It holds for all  $i \in [2\lambda]$  that  $\text{crsGen}(1^\lambda) \approx_c \text{bindingCrsGen}(1^\lambda, i)$ , i.e., common reference strings generated by  $\text{crsGen}$  and  $\text{bindingCrsGen}$  are computationally indistinguishable.

**Definition 10 (Hash Proof System).** *Let  $\mathcal{L}_z \subseteq \mathcal{M}_z$  be an NP-language residing in a universe  $\mathcal{M}_z$ , both parameterized by some parameter  $z$ . Moreover, let  $\mathcal{L}_z$  be characterized by an efficiently computable witness-relation  $\mathcal{R}$ , namely, for all  $x \in \mathcal{M}_z$  it holds that  $x \in \mathcal{L}_z \Leftrightarrow \exists w : \mathcal{R}(x, w) = 1$ . A hash proof system HPS for  $\mathcal{L}_z$  consists of three algorithms  $\text{KeyGen}$ ,  $\text{Hpublic}$  and  $\text{Hsecret}$  with the following syntax.*

- $(\text{pk}, \text{sk}) \leftarrow \text{KeyGen}(1^\lambda, z)$ : takes as input the security parameter  $\lambda$  and a parameter  $z$ , and outputs a public-key and secret key pair  $(\text{pk}, \text{sk})$ .
- $y \leftarrow \text{Hpublic}(\text{pk}, x, w)$ . Takes as input a public key  $\text{pk}$ , an instance  $x \in \mathcal{L}_z$ , and a witness  $w$ , and outputs a value  $y$ .
- $y \leftarrow \text{Hsecret}(\text{sk}, x)$ : takes as input a secret key  $\text{sk}$  and an instance  $x \in \mathcal{M}_z$ , and outputs a value  $y$ .

We require the following properties of a hash proof system.



1. **Perfect Completeness:** For all  $z$ , for all  $(\text{pk}, \text{sk})$  in the generated by  $\text{KeyGen}(1^\lambda, z)$ , and for all  $x \in \mathcal{L}_z$  having witness  $w$  (i.e.,  $\mathcal{R}(x, w) = 1$ ), it holds that:

$$\text{Hpublic}(\text{pk}, x, w) = \text{Hsecret}(\text{sk}, x)$$

2. **Perfect Soundness:** For every  $z$  and every  $x \in \mathcal{M}_z$ , let  $(\text{pk}, \text{sk})$  generated by  $\text{KeyGen}(1^\lambda, z)$ , then it holds that:

$$(z, \text{pk}, \text{Hsecret}(\text{sk}, x)) \equiv (z, \text{pk}, u)$$

where  $u$  is distributed uniformly random in the range of  $\text{Hsecret}$ .

Furthermore, we recall some notions, which are used in the description of the LOT construction in [7]. We denote by  $\hat{\mathbf{M}} = g^{\mathbf{M}} \in \mathbb{G}^{m \times n}$  the element-wise exponentiation of  $g$  with the elements of  $\mathbf{M}$ . We also define  $\hat{\mathbf{L}} = \hat{\mathbf{H}}^{\mathbf{M}} \in \mathbb{G}^{m \times k}$ , where  $\hat{\mathbf{H}} \in \mathbb{G}^{m \times n}$  and  $\mathbf{M} \in \mathbb{Z}_p^{n \times k}$  as follows: each element  $\hat{\mathbf{L}}_{i,j} = \prod_{k=1}^n \hat{\mathbf{H}}_{i,k}^{\mathbf{M}_{k,j}}$ . This is an abuse of notation, but intuitively this operation corresponds to matrix multiplication “in the exponent”.

**LOT with Factor-2 Compression.** In this part, we will present initially the constructions presented in [7] of SSBH and HPS, followed by their factor-2 compression LOT.

**SSB Hash Function.** Let  $n$  be an integer such that  $n = 2\lambda$ , and let  $(\mathbf{G}, \cdot)$  be a cyclic group of order  $p$  and with generator  $g$ . Let  $\mathbf{T}_i \in \mathbb{Z}_p^{2 \times n}$  be a matrix which is zero everywhere except the  $i$ -th column, which is equal to  $\mathbf{t} = (0, 1)^\top$ . The three algorithms of the SSB hash function are defined below.

$\text{crsGen}(1^\lambda):$ $\mathbf{H} \leftarrow_{\$} \mathbb{Z}_p^{2 \times n}$ $\hat{\mathbf{H}} \leftarrow g^{\mathbf{H}}$ return $\hat{\mathbf{H}}$	$\text{bindingCrsGen}(1^\lambda, i):$ $\mathbf{w} = \{(1, w_2)^\top \mid w_2 \leftarrow_{\$} \mathbb{Z}_p\}$ $\mathbf{a} \leftarrow_{\$} \mathbb{Z}_p^n$ $\mathbf{A} \leftarrow \mathbf{w} \cdot \mathbf{a}^\top$ $\mathbf{H} \leftarrow \mathbf{T}_i + \mathbf{A}$ $\hat{\mathbf{H}} \leftarrow g^{\mathbf{H}}$ return $\hat{\mathbf{H}}$	$\text{Hash}(\text{crs}, x):$ $\mathbf{y} \leftarrow \hat{\mathbf{H}}^{\mathbf{x}}$ return $y$
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Note that in the definition of  $\text{Hash}$ ,  $x \in \{0, 1\}^{2\lambda}$  needs to be parsed in a vector  $\mathbf{x} \in \mathbb{Z}_p^n$ . Furthermore, the output  $\mathbf{y} \in \mathbb{G}^2$  has to be returned parsed as a binary string  $y$ . Thus, in order to achieve factor-2 compression, i.e.  $y \in \{0, 1\}^\lambda$ , a bit-representation for a group element in  $\mathbb{G}$  of size  $\frac{\lambda}{2}$  is required.

**Hash Proof System.** Fix a matrix  $\hat{\mathbf{H}} \in \mathbb{G}^{2 \times n}$  and an index  $i \in [n]$ . Let  $\text{HPS} = (\text{KeyGen}, \text{Hpublic}, \text{Hsecret})$  be defined for the following language  $\mathcal{L}_{\hat{\mathbf{H}}, i}$ :

$$\mathcal{L}_{\hat{\mathbf{H}}, i} = \{(\hat{y}, b) \in \mathbb{G}^2 \times \{0, 1\} \mid \exists x \in \mathbb{Z}_p^n \text{ s.t. } \hat{y} = \hat{\mathbf{H}}^x \wedge x_i = b\}$$

Furthermore, for the ease of explanation, it would be convenient to work with a matrix  $\hat{\mathbf{H}}' = \begin{pmatrix} \hat{\mathbf{H}} \\ g^{e_i^\top} \end{pmatrix}$  where  $g^{e_i^\top} \in \mathbb{Z}_p^n$  is the  $i$ -th unit vector, with all elements equal to 0 except the  $i$ -th one which is equal to 1. The three algorithms of the HPS are defined as follows.

$\underline{\text{KeyGen}(1^\lambda, (\hat{\mathbf{H}}, i))}$ $r \leftarrow \mathbb{Z}_p^3$ $\hat{h} \leftarrow ((\hat{\mathbf{H}}')^\top)^r$ $pk \leftarrow \hat{h}, sk \leftarrow r$ <b>return</b> $(pk, sk)$	$\underline{\text{Hpublic}(pk, (\hat{h}, b), x)}$ $\hat{z} \leftarrow (pk^\top)^x = (\hat{h}^\top)^x$ <b>return</b> $\hat{z}$	$\underline{\text{Hsecret}(sk, (\hat{h}, b))}$ $\hat{y}' \leftarrow \begin{pmatrix} \hat{y} \\ g^b \end{pmatrix}$ $\hat{z} \leftarrow ((\hat{y}')^\top)^{sk} = ((\hat{y}')^\top)^r$ <b>return</b> $\hat{z}$
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**LOT Scheme.** Let  $\text{SSBH} := (\text{SSBH.crsGen}, \text{SSBH.bindingCrsGen}, \text{SSBH.Hash})$  be the SSB hash function with domain  $\mathcal{D} = \mathbb{Z}_p$ . Also, abstractly let the associated hash proof system be  $\text{HPS} = (\text{HPS.KeyGen}, \text{HPS.Hpublic}, \text{HPS.Hsecret})$  for the language  $\mathcal{L}_{\text{crs}, i} = \{(\text{digest}, b) \in \{0, 1\}^\lambda \times \{0, 1\} \mid \exists D \in \mathcal{D}^{2\lambda} : \text{SSBH.Hash}(\text{crs}, D) = \text{digest} \wedge D[i] = b\}$ .

The LOT scheme  $\ell\text{OT} := (\text{crsGen}, \text{Compress}, \text{Enc}, \text{Dec})$  follows:

<ul style="list-style-type: none"> <li>– <math>\underline{\text{crsGen}(1^\lambda)}</math>:  <math>\text{crs} \leftarrow \text{SSBH.crsGen}(1^\lambda)</math>  <b>return</b> <math>\text{crs}</math> </li> <li>– <math>\underline{\text{Compress}(\text{crs}, D \in \{0, 1\}^{2\lambda})}</math>:  <math>\text{digest} \leftarrow \text{SSBH.Hash}(\text{crs}, D)</math>  <math>\hat{D} \leftarrow (D, \text{digest})</math>  <b>return</b> <math>(\hat{D}, \text{digest})</math> </li> <li>– <math>\underline{\text{Enc}(\text{crs}, \text{digest}, L, m_0, m_1)}</math>:            Let HPS be the hash-proof system for the language <math>\mathcal{L}_{\text{crs}, L}</math>  <math>(pk, sk) \leftarrow \text{HPS.KeyGen}(1^\lambda, (\text{crs}, L))</math>  <math>c_0 \leftarrow m_0 \oplus \text{HPS.Hsecret}(sk, (\text{digest}, 0))</math>  <math>c_1 \leftarrow m_1 \oplus \text{HPS.Hsecret}(sk, (\text{digest}, 1))</math>  <math>e \leftarrow (pk, c_0, c_1)</math>  <b>return</b> <math>e</math> </li> <li>– <math>\underline{\text{Dec}^{\hat{D}}(\text{crs}, e, L)}</math>:  <math>e = (pk, c_0, c_1), \hat{D} = (D, \text{digest})</math>  <math>b \leftarrow D[L]</math>  <math>m \leftarrow c_b \oplus \text{HPS.Hpublic}(pk, (\text{digest}, b), D)</math>  <b>return</b> <math>m</math> </li> </ul>
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The above LOT protocol has factor-2 compression and satisfies the correctness and sender privacy requirements.

**LOT for arbitrarily large input databases.** The authors then provide a construction to bootstrap an  $\ell$ OT scheme with factor-2 compression into an LOT scheme with an arbitrary compression factor, which can compress a database of an arbitrary (a priori unbounded polynomial in  $\lambda$ ) size  $M$ . They achieved such generalization through *Merkle Trees*.

We briefly recall their approach. Assume for ease of exposition that  $M = 2d \cdot \lambda$ . First, the construction partitions the arbitrarily large database  $D \in \{0, 1\}^M$  into strings of length  $2\lambda$  (*leaves*). Then each of these strings is compressed into a new string of length  $\lambda$  via the factor-2 LOT Compress algorithm (*node*). Next, such strings are paired, forming new strings of length  $2\lambda$ , which can be compressed, in turn, through Hash, and so on. This process forms a Merkle tree structure. The Compress function of the general LOT scheme will exploit such structure, in fact it will compute  $(\hat{D}, \text{digest})$ , where  $\hat{D}$  is the entire Merkle tree, and  $\text{digest}$  is the root of the tree.

Now, it is easy to see that, in order to verify that a database  $D$ , with hash root  $\text{digest}$ , has a certain value  $b$  at a location  $L$ , there is no need to provide the entire Merkle tree. Instead, it is sufficient to provide a path of siblings from the  $\text{digest}$  to the leaf that contains location  $L$ . It can then be easily verified if the hash values from the leaf to the root are correct.

## C.2 Formal Description for $\text{NC}^1$ Construction in [4]

### Formal description.

- $(\text{msk}, \text{mpk}) \leftarrow_{\$} \text{FE.Setup}(1^\lambda, 1^k, 1^d)$ : let  $d$  stand for the circuit depth,  $k$  stand for the length of the supported inputs and  $\lambda$  for the security parameter.

for  $i \leftarrow 1, \dots, d - 1$  :

$$\mathbf{a}^i \leftarrow_{\$} \mathcal{R}_{p_i}^{L^i}$$

The set  $\{p_i : i \leftarrow 1, \dots, d\}$  stands for a set of  $d$  primes, while  $L^i$  denotes the size of an encoding (we assume they are a priori known). Then, a Lin-FE scheme is instantiated:

$$(\text{Lin-FE.mpk}, \text{Lin-FE.msk}) \leftarrow_{\$} \text{Lin-FE.Setup}(1^\lambda)$$

where  $\text{Lin-FE.mpk} \leftarrow (\mathbf{w}, \mathbf{a}^d)$ . The following variables are set and returned:

$$\text{mpk} \leftarrow (\mathbf{a}^1, \dots, \mathbf{a}^d, \mathbf{w})$$

$$\text{msk} \leftarrow \text{Lin-FE.msk}$$

- $\text{FE.KGen}(\text{msk}, \mathcal{C})$ : given a function  $\mathcal{C}$  represented as a circuit of depth  $d$ :

$$\text{PK}_{\mathcal{C}} \leftarrow_{\$} \text{Eval}_{\text{PK}}(\text{mpk}, \mathcal{C})$$

Invoke key-derivation for Lin-FE to obtain  $\text{sk}_{\mathcal{C}}$ <sup>7</sup> and return it:

$$\text{sk}_{\mathcal{C}} \leftarrow \text{Lin-FE.KGen}(\text{msk}, \text{PK}_{\mathcal{C}})$$

- $\text{FE.Enc}(\text{mpk}, M = [M_1, \dots, M_k])$ : first, for each  $M_j$ , compute its encodings, recursively, as shown in Figure 2 for all multiplicative levels  $1 \rightarrow d$ :

$$\mathcal{C}_j^i \leftarrow \mathcal{E}^i(\dots \mathcal{E}^1(M_j) \dots \cdot s \dots), \forall i \in [d] .$$

Then, set  $\mathbf{d}$  as follows:

$$\mathbf{d} \leftarrow \mathbf{w} \cdot s + p_{d-1} \cdot \mu .$$

Finally, set the ciphertext corresponding to  $M$  as  $\text{CT}_M \leftarrow (\{\mathcal{C}^i\}_{i \in [d]}, \mathbf{d})$ .

- $\text{FE.Dec}(\text{sk}_{\mathcal{C}}, \text{CT}_M)$ : compute

$$\text{CT}_{\mathcal{C}(M)} \leftarrow \text{Eval}_{\mathcal{C}}(\{\mathcal{C}^i\}_{i \in [d]}, \mathcal{C})$$

which is equivalent to:

$$\text{CT}_{\mathcal{C}(M)} \leftarrow \text{PK}_{\mathcal{C}} \cdot s + p_{d-1} \cdot \eta^{d-1} + \dots + p_0 \cdot \eta^0 + \mathcal{C}(M) \in \mathcal{R}_{p_d} .$$

Compute also:

$$(\text{PK}_{\mathcal{C}} \cdot s + p_{d-1} \cdot \eta') \leftarrow \text{Lin-FE.Dec}(\text{sk}_{\mathcal{C}}, \mathbf{d}) .$$

Subtract the last two equations and remove the noise terms to obtain  $\mathcal{C}(M)$ .

More details on  $\text{Eval}_{\text{PK}}$  and  $\text{Eval}_{\text{PK}}$  are given below. We note that correctness should follow from the description of these algorithms.

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<sup>7</sup> Note that  $\mathbf{w}^\top \cdot \text{sk}_{\mathcal{C}} = \text{PK}_{\mathcal{C}}$ .