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Joint Bit Allocation and Hybrid Beamforming Optimization for Energy Efficient Millimeter Wave MIMO Systems

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Abstract

In this paper, we aim to design highly energy efficient end-to-end communication for millimeter wave multiple-input multiple-output systems. This is done by jointly optimizing the digital-to-analog converter (DAC)/analog-to-digital converter (ADC) bit resolutions and hybrid beamforming matrices. The novel decomposition of the hybrid precoder and the hybrid combiner to three parts is introduced at the transmitter (TX) and the receiver (RX), respectively, representing the analog precoder/combiner matrix, the DAC/ADC bit resolution matrix and the baseband precoder/combiner matrix. The unknown matrices are computed as a solution to the matrix factorization problem where the optimal fully digital precoder or combiner is approximated by the product of these matrices. A novel and efficient solution based on the alternating direction method of multipliers is proposed to solve these problems at both the TX and the RX. The simulation results show that the proposed solution, where the DAC/ADC bit allocation is dynamic during operation, achieves higher energy efficiency when compared with existing benchmark techniques that use fixed DAC/ADC bit resolutions.

Index Terms

DAC/ADC bit resolution and hybrid beamforming optimization, energy efficiency maximization, millimeter wave MIMO, beyond 5G wireless.

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I. Introduction

ILLIMETER WAVE (mmWave) spectrum is an attractive alternative to the densely occupied microwave spectrum range of 300 MHz to 6 GHz for next generation wireless communication systems. The advantages of using a mmWave frequency band are increased capacity, lower latency, high mobility and reliability, and lower infrastructure costs [2]–[4]. The higher path loss associated with mmWave spectrum can be compensated by using large scale antenna arrays leading to a multiple-input multiple-output (MIMO) system. Implementing fully digital beamforming in mmWave MIMO systems provides high throughput but has high complexity and low energy efficiency (EE). A simpler alternative is a fully analog beamforming approach which was discussed in [5] but cannot implement multi-stream spatial communication due to the use of a single radio frequency (RF) chain.

Analog/digital (A/D) hybrid beamforming MIMO architectures implement both digital and analog units to overcome these issues. The hardware complexity and power consumption is reduced through using fewer RF chains and it can support multi-stream communication with high spectral efficiency (SE) [6]–[15]. Such systems can be also optimized to achieve high EE gains [16]–[18]. An alternative solution to reduce the power consumption and hardware complexity is by decreasing the bit resolution [19] of the digital-to-analog converters (DACs) and the analog-to-digital converters (ADCs). Given the distinct system and channel model characteristics at mmWave compared to microwave, the EE and SE performance needs to be analyzed for the A/D hybrid beamforming architecture with low resolution sampling.

A. Literature Review

To observe the effect of ADC resolution and bandwidth on rate, an additive quantization noise model (AQNM) is considered in [20] for a mmWave MIMO system under a receiver (RX) power constraint. Reference [21] uses AQNM and shows the significance of low resolution ADCs on decreasing the rate. Recent work on A/D hybrid MIMO systems with low resolution sampling dynamically adjusts the ADC resolution [22]. Most of the literature such as in [20]–[26] imposes low resolution only at the RX side, and mostly assumed a fully digital or hybrid TX with high resolution DACs. However, there is a need to conduct research on optimizing the bit resolution problem for the TX side as well.

Furthermore, the existing literature mostly develops systems based on high resolution ADCs with a small number of RF chains or low resolution ADCs with a large number of RF chains.

Either way, only fixed resolution DACs/ADCs are taken into account. References [16], [17] consider EE optimization problems for A/D hybrid transceivers but with fixed and high resolution at the DACs/ADCs. The power model in [16] takes into account the power consumed at every RF chain and a constant power term for site-cooling, baseband processing and synchronization at the TX and [17] considers the RF hardware losses and some computational power expenditure.

Some approaches have been applied in A/D hybrid mmWave MIMO systems for EE maximization and low complexity with both full and low resolution sampling cases [18], [27]. Reference [18] proposes an energy efficient A/D hybrid beamforming framework with a novel architecture for a mmWave MIMO system. The number of active RF chains are optimized dynamically by fractional programming to maximize EE performance but the DAC/ADC bit resolutions are fixed. Reference [27] proposes a novel EE maximization technique that selects the best subset of the active RF chains and DAC resolution which can also be extended to low resolution ADCs at the RX. Reference [23] suggests implementing fixed and low resolution ADCs with a small number of RF chains. Reference [24] works on the idea of a mixed-ADC architecture where a better energy-rate trade off is achieved by combining low and high resolution ADCs, but still with a fixed resolution for each ADC and without considering A/D hybrid beamforming. An A/D hybrid beamforming system with fixed and low resolution ADCs has been analyzed for channel estimation in [25].

One can implement varying resolution ADCs at the RX [26] which may provide a better solution than the RX with fixed and low resolution ADCs. Similarly, exploring low resolution DACs at the TX can also help reduce the power consumption. Thus, research that is focused on ADCs at the RX can also be applied to the TX DACs considering the TX specific system model parameters. Similar to using different ADC resolutions at the RX [26], which could provide a better solution than fixed low resolution ADCs, one can design a variable DAC resolution TX. Extra care is needed when deciding the number of bits used as the total DAC/ADC power consumption can be dominated by only a few high resolution DACs/ADCs. From [28], we notice that a good trade off between the power consumption and the performance may be to consider the range of 1-8 bits for I- and Q-channels, where 8-bit represents the full-bit resolution DACs/ADCs.

Reference [29] uses low resolution DACs for a single user MIMO system while [30] employs low resolution DACs at the base station for a narrowband multi-user MIMO system. Reference [31] also discusses fixed and low resolution DACs architecture for multi-user MIMO systems.

Reference [32] considers a single user MIMO system with quantized hybrid precoding including the RF quantized noise term beside the additive white Gaussian noise (AWGN) while evaluating EE and SE performance. The existing literature still does not consider adjusting the resolution associated with DACs/ADCs dynamically. It is possible to consider both the TX and the RX simultaneously where we can design an optimization problem to find the optimal number of quantized bits to achieve high EE performance. When designing for high EE, the complexity of the solution also needs to be taken into account while providing improvements over the existing literature.

B. Contributions

This paper designs an optimal EE solution for a mmWave A/D hybrid MIMO system by introducing a novel TX decomposition of the A/D hybrid precoder to three parts representing the analog precoder matrix, the DAC bit resolution matrix and the digital precoder matrix, respectively. A similar decomposition at the RX represents the analog combiner matrix, the ADC bit resolution matrix and the digital combiner matrix. Our aim is to minimize the distance between the decomposition, which is expressed as the product of three matrices, and the corresponding fully digital precoder or combiner matrix. The joint problem is decomposed into a series of sub-problems which are solved using the alternating direction method of multipliers (ADMM). We also implement an exhaustive search approach [16] to evaluate the upper bound for EE maximization.

In addition to [1], where bit allocation and hybrid combining are addressed at the RX, the main contributions of this paper can be listed as follows:

- This paper designs an optimal EE solution for a mmWave A/D hybrid beamforming MIMO system by introducing a novel matrix decomposition that is applied to the precoder and the combiner matrices at the TX and the RX, respectively. This decomposition defines three matrices, which are the analog beamforming matrix, the bit resolution matrix and the baseband beamforming matrix at both the TX and the RX. These matrices are obtained by the solution of an EE maximization problem.
- The joint TX/RX problem is decoupled into two sub-problems dealing with the TX and the RX separately. The corresponding problems at the TX and the RX are solved by the alternating minimization framework such as ADMM [33] to obtain the unknown precoder/combiner and DAC/ADC bit resolution matrices.

• This work jointly optimizes the hybrid beamforming and DAC/ADC bit resolution matrices, unlike the existing approaches that optimize either DAC/ADC bit resolution or hybrid beamforming matrices. Moreover, the proposed design has high flexibility, given that the analog precoder/combiner is codebook-free, thus there is no restriction on the angular vectors and different bit resolutions can be assigned to each DAC/ADC.

C. Notation and Organization

A, a and a stand for a matrix, a vector, and a scalar, respectively. The trace, transpose and complex conjugate transpose of \mathbf{A} are denoted as $\mathrm{tr}(\mathbf{A})$, \mathbf{A}^T and \mathbf{A}^H , respectively; $\|\mathbf{A}\|_F$ represents the Frobenius norm of \mathbf{A} ; |a| represents the determinant of a; \mathbf{I}_N represents $N \times N$ identity matrix; $\mathcal{CN}(\mathbf{a};\mathbf{A})$ denotes a complex Gaussian vector having mean a and covariance matrix \mathbf{A} ; \mathbb{C} , \mathbb{R} and \mathbb{R}^+ denote the sets of complex numbers, real numbers and positive real numbers, respectively; $\mathbf{X} \in \mathbb{C}^{A \times B}$ and $\mathbf{X} \in \mathbb{R}^{A \times B}$ denote $A \times B$ size \mathbf{X} matrix with complex and real entries, respectively; $[\mathbf{A}]_k$ denotes the k-th column of matrix \mathbf{A} while $[\mathbf{A}]_{kl}$ the matrix entry at the k-th row and l-th column; the indicator function $\mathbb{1}_{\mathcal{S}}\{\mathbf{A}\}$ of a set \mathcal{S} that acts over a matrix \mathbf{A} is defined as $0 \ \forall \ \mathbf{A} \in \mathcal{S}$ and $\infty \ \forall \ \mathbf{A} \notin \mathcal{S}$.

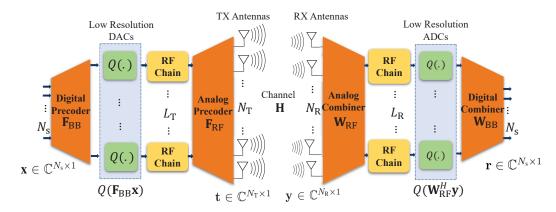
Section II presents the channel and system models where the channel model is based on a mmWave channel setup and the system model defines the low resolution quantization at both the TX and the RX. Sections III and IV present the problem formulation for the proposed technique at the TX and the RX, respectively, and the solution to obtain an energy efficient system. Section V verifies the proposed technique through simulation results and Section VI concludes the paper.

II. MMWAVE A/D HYBRID MIMO SYSTEM

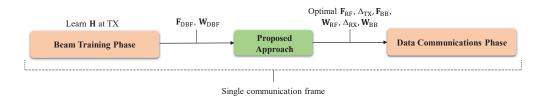
A. MmWave Channel Model

MmWave channels can be modeled by a narrowband clustered channel model due to different channel settings such as the number of multipaths, amplitudes, etc., with $N_{\rm cl}$ clusters and $N_{\rm ray}$ propagation paths in each cluster [6]. Considering a single user mmWave system with $N_{\rm T}$ antennas at the TX, transmitting $N_{\rm s}$ data streams to $N_{\rm R}$ antennas at the RX, the mmWave channel matrix can be written as follows:

$$\mathbf{H} = \sqrt{\frac{N_{\mathsf{T}} N_{\mathsf{R}}}{N_{\mathsf{cl}} N_{\mathsf{ray}}}} \sum_{i=1}^{N_{\mathsf{cl}}} \sum_{l=1}^{N_{\mathsf{ray}}} \alpha_{il} \mathbf{a}_{\mathsf{R}}(\phi_{il}^r) \mathbf{a}_{\mathsf{T}}(\phi_{il}^t)^H, \tag{1}$$



(a) A mmWave A/D hybrid MIMO system with varying DAC/ADC bit resolutions at the TX/RX.



(b) Block diagram of the beam tracking phase and the data communications phase.

Fig. 1: System model for a mmWave A/D hybrid MIMO system with the proposed framework.

where $\alpha_{il} \in \mathcal{CN}(0, \sigma_{\alpha,i}^2)$ is the gain term with $\sigma_{\alpha,i}^2$ being the average power of the i^{th} cluster. Furthermore, $\mathbf{a}_{\mathrm{T}}(\phi_{il}^t)$ and $\mathbf{a}_{\mathrm{R}}(\phi_{il}^r)$ represent the normalized transmit and receive array response vectors [6], where ϕ_{il}^t and ϕ_{il}^r denote the azimuth angles of departure and arrival, respectively. We use uniform linear array (ULA) antennas for simplicity and model the antenna elements at the RX as ideal sectored elements [34]. We assume that the channel state information (CSI) is known at both the TX and the RX.

B. A/D Hybrid MIMO System Model

Based on the A/D hybrid beamforming scheme in the large scale mmWave MIMO communication systems, the number of TX RF chains $L_{\rm T}$ follows the limitation $N_{\rm s} \leq L_{\rm T} \leq N_{\rm T}$ and similarly for $L_{\rm R}$ RF chains at the RX, $N_{\rm s} \leq L_{\rm R} \leq N_{\rm R}$ [6], [7]. As shown in Fig. 1 (a), the matrices $F_{\rm RF} \in \mathbb{C}^{N_{\rm T} \times L_{\rm T}}$ and $F_{\rm BB} \in \mathbb{C}^{L_{\rm T} \times N_{\rm s}}$ denote the analog precoder and baseband precoder matrices, respectively. Similarly, the matrices $W_{\rm RF} \in \mathbb{C}^{N_{\rm R} \times L_{\rm R}}$ and $W_{\rm BB} \in \mathbb{C}^{L_{\rm R} \times N_{\rm s}}$ denote the analog combiner and baseband combiner matrices, respectively. The analog precoder and combiner matrices, $F_{\rm RF}$ and $W_{\rm RF}$, are based on phase shifters, i.e., the elements that have

unit modulus and continuous phase. Thus, $\mathbf{F}_{\mathrm{RF}} \in \mathcal{F}^{N_{\mathrm{T}} \times L_{\mathrm{T}}}$ and $\mathbf{W}_{\mathrm{RF}} \in \mathcal{W}^{N_{\mathrm{R}} \times L_{\mathrm{R}}}$ where the set \mathcal{F} and \mathcal{W} represent the set of possible phase shifts in \mathbf{F}_{RF} and \mathbf{W}_{RF} , respectively. The sets \mathcal{F} and \mathcal{W} for variables f and w, respectively, are defined as $\mathcal{F} = \{f \in \mathbb{C} \mid |f| = 1\}$ and $\mathcal{W} = \{w \in \mathbb{C} \mid |w| = 1\}$.

Note that, we optimize the DAC and ADC resolution and the precoder and combiner matrices at the TX and the RX on a frame-by-frame basis. As shown in Fig. 1 (b), we consider two stages in the system model: i) the beam training phase, and ii) the data communications phase. In stage i), firstly, the channel H is computed which provides us the optimal beamforming matrices, i.e., \mathbf{F}_{DBF} at the TX and \mathbf{W}_{DBF} at the RX. In stage ii), the optimal precoding and DAC bit resolution matrices \mathbf{F}_{RF} , \mathbf{F}_{BB} and $\mathbf{\Delta}_{TX}$ at the TX, respectively, and the optimal combining and ADC bit resolution matrices \mathbf{W}_{RF} , \mathbf{W}_{BB} and $\mathbf{\Delta}_{RX}$ at the RX are obtained. These two phases consist of one communication frame where the frame duration is smaller than the channel coherence time. Furthermore, if we assume that the TX/RX is active for stage i) a small proportion of time, for example, < 10%, then the overall transmit energy consumption is dominated by stage ii).

We consider the linear AQNM to represent the distortion of quantization [20]. Given that $Q(\cdot)$ denotes a uniform scalar quantizer then for the scalar complex input $x \in \mathbb{C}$ that is applied to both the real and imaginary parts, we have, $Q(x) \approx \delta x + \epsilon$, where $\delta = \sqrt{1 - \frac{\pi\sqrt{3}}{2} 2^{-2b}} \in [m, M]$ is the multiplicative distortion parameter for a bit resolution equal to b [37], where m and M denote the minimum and maximum value of the range. The resolution parameter b is denoted as $b_i^t \forall i = 1, \ldots, L_T$ and $b_i^r \forall i = 1, \ldots, L_R$ at the TX and the RX, respectively. Note that the introduced error in the above linear approximation decreases for larger resolutions. However, our proposed solution focuses on EE maximization and this linear approximation does not impact the performance significantly as observed from the simulation results in Section V. The parameter ϵ is the additive quantization noise with $\epsilon \sim \mathcal{CN}(0, \sigma_{\epsilon}^2)$, where $\sigma_{\epsilon} = \sqrt{1 - \frac{\pi\sqrt{3}}{2} 2^{-2b}} \sqrt{\frac{\pi\sqrt{3}}{2} 2^{-2b}}$. The matrices Δ_{TX} and Δ_{RX} represent diagonal matrices with values depending on the bit resolution of each DAC and ADC, respectively. Specifically, each diagonal entry of Δ_{TX} is given by:

$$[\mathbf{\Delta}_{\text{TX}}]_{ii} = \sqrt{1 - \frac{\pi\sqrt{3}}{2}} 2^{-2b_i^t} \in [m, M] \,\forall \, i = 1, \dots, L_{\text{T}},$$
 (2)

and each diagonal entry of Δ_{RX} is given by:

$$[\Delta_{\text{RX}}]_{ii} = \sqrt{1 - \frac{\pi\sqrt{3}}{2} 2^{-2b_i^T}} \in [m, M] \, \forall \, i = 1, \dots, L_{\text{R}},$$
 (3)

where, for simplicity, we assume that the range [m, M] is the same for each of the DACs/ADCs. The additive quantization noise for the DACs and ADCs are written as complex Gaussian vectors $\epsilon_{TX} \in \mathcal{CN}(\mathbf{0}, \mathbf{C}_{\epsilon T})$ and $\epsilon_{RX} \in \mathcal{CN}(\mathbf{0}, \mathbf{C}_{\epsilon R})$ [27] where $\mathbf{C}_{\epsilon T}$ and $\mathbf{C}_{\epsilon R}$ are the diagonal covariance matrices for DACs and ADCs, respectively. The covariance matrix entries are as follows:

$$[\mathbf{C}_{\epsilon \mathbf{T}}]_{ii} = \left(1 - \frac{\pi\sqrt{3}}{2} 2^{-2b_i^t}\right) \left(\frac{\pi\sqrt{3}}{2} 2^{-2b_i^t}\right) \forall i = 1, ..., L_{\mathbf{T}},\tag{4}$$

and

$$[\mathbf{C}_{\epsilon \mathbf{R}}]_{ii} = \left(1 - \frac{\pi\sqrt{3}}{2} 2^{-2b_i^r}\right) \left(\frac{\pi\sqrt{3}}{2} 2^{-2b_i^r}\right) \forall i = 1, ..., L_{\mathbf{R}}.$$
 (5)

Note that while optimizing the EE of the TX side, it is considered that the RX parameters, which includes the analog combiner matrix, the ADC bit resolution matrix and the baseband combiner matrix is known to the TX and vice-versa.

Let us consider $\mathbf{x} \in \mathbb{C}^{N_s \times 1}$ as the normalized data vector, then based on the AQNM, the vector containing the complex output of all the DACs can be expressed as follows:

$$Q(\mathbf{F}_{BB}\mathbf{x}) \approx \mathbf{\Delta}_{TX}\mathbf{F}_{BB}\mathbf{x} + \boldsymbol{\epsilon}_{TX} \in \mathbb{C}^{L_T \times 1},$$
 (6)

This leads us to the following linear approximation for the transmitted signal $\mathbf{t} \in \mathbb{C}^{N_T \times 1}$, as seen at the output of the A/D hybrid TX in Fig. 1 (a):

$$t = F_{RF} \Delta_{TX} F_{RR} x + F_{RF} \epsilon_{TX}. \tag{7}$$

After the effect of the wireless mmWave channel \mathbf{H} and the Gaussian noise \mathbf{n} with independent and identically distributed entries and complex Gaussian distribution, i.e., $\mathbf{n} \sim \mathcal{CN}(\mathbf{0}, \sigma_n^2 \mathbf{I}_{N_R})$, the received signal $\mathbf{y} \in \mathbb{C}^{N_R \times 1}$ is expressed as follows:

$$y = Ht + n = HF_{RF}\Delta_{TX}F_{BB}X + HF_{RF}\epsilon_{TX} + n.$$
 (8)

When the analog combiner matrix W_{RF} and ADC quantization based on AQNM are applied to the received signal y, we obtain the following:

$$Q(\mathbf{W}_{RF}^{H}\mathbf{y}) \approx \mathbf{\Delta}_{RX}^{H}\mathbf{W}_{RF}^{H}\mathbf{y} + \boldsymbol{\epsilon}_{RX} \in \mathbb{C}^{L_{R} \times 1}.$$
 (9)

After the application of the baseband combiner matrix \mathbf{W}_{BB} , the output signal $\mathbf{r} \in \mathbb{C}^{N_s \times 1}$ at the RX, as shown in Fig. 1 (a), can be expressed as follows:

$$\mathbf{r} = \mathbf{W}_{BB}^{H} \mathbf{\Delta}_{RX}^{H} \mathbf{W}_{RF}^{H} \mathbf{y} + \mathbf{W}_{BB}^{H} \boldsymbol{\epsilon}_{RX}. \tag{10}$$

Considering the A/D hybrid precoder matrix $\mathbf{F} = \mathbf{F}_{RF} \boldsymbol{\Delta}_{TX} \mathbf{F}_{BB} \in \mathbb{C}^{N_T \times N_s}$ and the A/D hybrid combiner matrix $\mathbf{W} = \mathbf{W}_{RF} \boldsymbol{\Delta}_{RX} \mathbf{W}_{BB} \in \mathbb{C}^{N_R \times N_s}$, we can express the RX output signal \mathbf{r} in (10) as follows:

$$\mathbf{r} = \mathbf{W}^{H}\mathbf{H}\mathbf{F}\mathbf{x} + \underbrace{\mathbf{W}^{H}\mathbf{H}\mathbf{F}_{RF}\boldsymbol{\epsilon}_{TX} + \mathbf{W}_{BB}^{H}\boldsymbol{\epsilon}_{RX} + \mathbf{W}^{H}\mathbf{n}}_{\boldsymbol{\eta}},$$
(11)

where η is the combined effect of the additive white Gaussian RX noise and quantization noise with $\eta \sim \mathcal{CN}(0, \mathbf{R}_{\eta})$, where $\mathbf{R}_{\eta} \in \mathbb{C}^{N_s \times N_s}$ is the combined noise covariance matrix where,

$$\mathbf{R}_{\eta} = \mathbf{W}^{H} \mathbf{H} \mathbf{F}_{RF} \mathbf{C}_{\epsilon T} \mathbf{F}_{RF}^{H} \mathbf{H}^{H} \mathbf{W} + \mathbf{W}_{RB}^{H} \mathbf{C}_{\epsilon R} \mathbf{W}_{BB} + \sigma_{n}^{2} \mathbf{W}^{H} \mathbf{W}. \tag{12}$$

C. Hybrid Beamforming with Full-bit Resolution

Before continuing to description of the proposed technique, let us briefly discuss the conventional hybrid beamforming design which aims to achieve high SE. These maximum SE hybrid beamformer expressions will be used in the analysis of the proposed technique sections.

The channel's singular value decomposition (SVD) is written as $\mathbf{H} = \mathbf{U}_{H} \mathbf{\Sigma}_{H} \mathbf{V}_{H}^{H}$, where $\mathbf{U}_{H} \in \mathbb{C}^{N_{R} \times N_{R}}$ and $\mathbf{V}_{H} \in \mathbb{C}^{N_{T} \times N_{T}}$ are unitary matrices. The rectangular matrix $\mathbf{\Sigma}_{H} \in \mathbb{R}^{N_{R} \times N_{T}}$ contains the singular values in decreasing order whose diagonal elements are non-negative real numbers and whose non-diagonal elements are zero. To maximize throughput, the fully digital precoder matrix \mathbf{F}_{DBF} at the TX consists of the N_{s} columns of the right singular matrix \mathbf{V}_{H} that correspond to the N_{s} largest singular values [6]. In the context of [6], a hybrid precoding solution that maximizes the information rate requires the decomposition $\mathbf{F}_{RF}\mathbf{F}_{BB}$ to be close to the fully digital precoder \mathbf{F}_{DBF} . Thus we can consider the Euclidean distance between the A/D hybrid precoder with full-bit resolution DACs, $\mathbf{F}_{RF}\mathbf{F}_{BB}$, and the channel's optimal fully digital precoder \mathbf{F}_{DBF} to optimize the A/D hybrid precoder matrices, which can be stated as follows [6], [7]:

$$(\mathbf{F}_{RF}^{opt}, \mathbf{F}_{BB}^{opt}) = \underset{\mathbf{F}_{RF}, \mathbf{F}_{BB}}{\min} \|\mathbf{F}_{DBF} - \mathbf{F}_{RF} \mathbf{F}_{BB}\|_{F}^{2},$$
subject to $\mathbf{F}_{RF} \in \mathcal{F}_{RF}, \|\mathbf{F}_{RF} \mathbf{F}_{BB}\|_{F}^{2} = P_{\text{max}},$

$$(13)$$

where P_{\max} is the maximum allocated power at the TX. \mathcal{F}_{RF} is a set of basis vectors $\mathbf{a}_{T}(\tilde{\phi}_{il}^{t})$ where $\tilde{\phi}_{il}^{t}$ are the angles from the discrete Fourier transform (DFT) codebook.

Similarly at the RX, the design problem for combining matrices with full-bit resolution ADCs can be written as,

$$(\mathbf{W}_{RF}^{opt}, \mathbf{W}_{BB}^{opt}) = \underset{\mathbf{W}_{RF}, \mathbf{W}_{BB}}{\operatorname{arg min}} \mathbb{E} \left[\left\| \mathbf{x} - \mathbf{W}_{BB}^{H} \mathbf{W}_{RF}^{H} \tilde{\mathbf{r}} \right\|_{2}^{2} \right],$$
subject to $\mathbf{W}_{RF} \in \mathcal{W}_{RF}$,
$$(14)$$

where W_{RF} is defined similarly to \mathcal{F}_{RF} for the TX. The vector $\tilde{\mathbf{r}} \in \mathbb{C}^{N_s \times 1}$ represents the received signal given by (10) for the case of full-bit resolution DACs and ADCs, i.e., $\tilde{\mathbf{r}} = \mathbf{W}_{BB}^H \boldsymbol{\Delta}_{RX}^H \mathbf{W}_{RF}^H (\mathbf{H}\mathbf{F}_{RF}\boldsymbol{\Delta}_{TX}\mathbf{F}_{BB}\mathbf{x} + \mathbf{H}\mathbf{F}_{RF}\boldsymbol{\epsilon}_{TX} + \mathbf{n}) + \mathbf{W}_{BB}^H \boldsymbol{\epsilon}_{RX}$, where $\boldsymbol{\Delta}_{TX} = \mathbf{I}_{L_T}$ and $\boldsymbol{\Delta}_{RX} = \mathbf{I}_{L_R}$. Following the steps in [6] and similar to the precoder, the minimum mean square error (MMSE) estimation problem may be further written as,

$$\tilde{\mathbf{W}}_{\mathrm{BB}}^{opt} = \underset{\tilde{\mathbf{W}}_{\mathrm{BB}}}{\operatorname{arg min}} \left\| \mathbb{E} [\tilde{\mathbf{r}} \tilde{\mathbf{r}}^{H}]^{\frac{1}{2}} \mathbf{W}_{\mathrm{DBF}} - \mathbb{E} [\tilde{\mathbf{r}} \tilde{\mathbf{r}}^{H}]^{\frac{1}{2}} \mathbf{D}_{\mathrm{R}} \tilde{\mathbf{W}}_{\mathrm{BB}} \right\|_{F}^{2}$$
subject to
$$\left\| \operatorname{diag}(\tilde{\mathbf{W}}_{\mathrm{BB}} \tilde{\mathbf{W}}_{\mathrm{BB}}^{H}) \right\|_{0} = L_{\mathrm{R}},$$
(15)

where $\mathbf{D}_{R} \in \mathbb{C}^{N_{R} \times L_{R}}$ is the DFT matrix from the beamspace representation of the channel [35], [36] and as similarly shown in [18], and $\tilde{\mathbf{W}}_{BB}$ is a $L_{R} \times N_{s}$ matrix. The exact solution to (15) yields \mathbf{W}_{DBF}^{H} as follows [6]:

$$\mathbf{W}_{\mathrm{DBF}}^{H} = \left(\mathbf{F}_{\mathrm{BB}}^{H} \mathbf{F}_{\mathrm{RF}}^{H} \mathbf{H} \mathbf{H}^{H} \mathbf{F}_{\mathrm{RF}} \mathbf{F}_{\mathrm{BB}} + \sigma_{\mathrm{n}}^{2} N_{\mathrm{s}} \mathbf{I}_{N_{\mathrm{s}}}\right)^{-1} \mathbf{F}_{\mathrm{BB}}^{H} \mathbf{F}_{\mathrm{RF}}^{H} \mathbf{H}^{H}. \tag{16}$$

In the following two sections, we proceed by including the low resolution sampling at both the TX and the RX, provide the problem formulation and propose the ADMM approach to obtain the optimal bit resolution and hybrid beamforming matrices.

III. JOINT DAC BIT ALLOCATION AND A/D HYBRID PRECODING DESIGN

Let us consider a point-to-point MIMO system with a linear quantization model. We define the EE as the ratio of the information rate R and the total consumed power P [38] as:

$$EE \triangleq \frac{R}{P} \text{ (bits/Hz/J)}.$$
 (17)

The information rate when considering both TX and RX is defined as:

$$R \triangleq \log_2 \left| \mathbf{I}_{N_s} + \frac{\mathbf{R}_{\eta}^{-1}}{N_s} \mathbf{W}^H \mathbf{H} \mathbf{F} \mathbf{F}^H \mathbf{H}^H \mathbf{W} \right| \text{ (bits/s/Hz)}, \tag{18}$$

where $\mathbf{F}=\mathbf{F}_{RF}\boldsymbol{\Delta}_{TX}\mathbf{F}_{BB}$ and $\mathbf{W}=\mathbf{W}_{RF}\boldsymbol{\Delta}_{RX}\mathbf{W}_{BB}.$

Similar to the power model at the TX in [27], the total consumed power for the system is expressed as:

$$P \triangleq P_{\text{TX}}(\mathbf{F}_{\text{RF}}, \mathbf{\Delta}_{\text{TX}}, \mathbf{F}_{\text{BB}}) + P_{\text{RX}}(\mathbf{\Delta}_{\text{RX}}) \text{ (W)}, \tag{19}$$

where the power consumption at the TX is as follows:

$$P_{\text{TX}}(\mathbf{F}_{\text{RF}}, \boldsymbol{\Delta}_{\text{TX}}, \mathbf{F}_{\text{BB}}) = \text{tr}(\mathbf{F}\mathbf{F}^H) + P_{\text{DT}}(\boldsymbol{\Delta}_{\text{TX}}) + N_{\text{T}}P_{\text{T}} + N_{\text{T}}L_{\text{T}}P_{\text{PT}} + P_{\text{CT}}(\mathbf{W}), \tag{20}$$

where P_{PT} is the power per phase shifter, P_{T} is the power per antenna element, $P_{\text{DT}}(\Delta_{\text{TX}})$ is the power associated with the total quantization operation at the TX, and following (2) and [20], we have

$$P_{\text{DT}}(\boldsymbol{\Delta}_{\text{TX}}) = P_{\text{DAC}} \sum_{i=1}^{L_{\text{T}}} 2^{b_i} = P_{\text{DAC}} \sum_{i=1}^{L_{\text{T}}} \left(\frac{\pi\sqrt{3}}{2(1 - [\boldsymbol{\Delta}_{\text{TX}}]_{ii}^2)} \right)^{\frac{1}{2}} (\mathbf{W}), \tag{21}$$

where P_{DAC} is the power consumed per bit in the DAC and P_{CT} is the power required by all circuit components at the TX. Similarly, the total power consumption at the RX is,

$$P_{\text{RX}}(\Delta_{\text{RX}}) = P_{\text{DR}}(\Delta_{\text{RX}}) + N_{\text{R}}P_{\text{R}} + N_{\text{R}}L_{\text{R}}P_{\text{PR}} + P_{\text{CR}} \text{ (W)}, \tag{22}$$

where, at the RX, P_{PR} is the power per phase shifter, P_{R} is the power per antenna element, P_{DR} is the power associated with the total quantization operation, and following (3) and [20], we have

$$P_{\rm DR}(\boldsymbol{\Delta}_{\rm RX}) = P_{\rm ADC} \sum_{i=1}^{L_{\rm R}} 2^{b_i} = P_{\rm ADC} \sum_{i=1}^{L_{\rm R}} \left(\frac{\pi\sqrt{3}}{2(1 - [\boldsymbol{\Delta}_{\rm RX}]_{ii}^2)} \right)^{\frac{1}{2}} (W), \tag{23}$$

where P_{ADC} is the power consumed per bit in the ADC and P_{CR} is the power required by all RX circuit components.

The maximization of EE is given by:

$$\max_{\mathbf{F}_{RF}, \mathbf{\Delta}_{TX}, \mathbf{F}_{BB}, \mathbf{W}_{RF}, \mathbf{\Delta}_{RX}, \mathbf{W}_{BB}} \frac{R}{P}$$
subject to $\mathbf{F}_{RF} \in \mathcal{F}^{N_{T} \times L_{T}}, \mathbf{\Delta}_{TX} \in \mathcal{D}_{TX}^{L_{T} \times L_{T}}, \mathbf{W}_{RF} \in \mathcal{W}^{N_{R} \times L_{R}}, \mathbf{\Delta}_{RX} \in \mathcal{D}_{RX}^{L_{R} \times L_{R}},$ (24)

when the rate R is given by (18) and the power P in (19). This problem is in general intractable due to the coupling of the TX-RX design [39]. A common solution is to decouple the designs at the TX and the RX side. Thus firstly, the optimal DAC bit resolution matrix Δ_{TX} and hybrid precoding matrices F_{RF} and F_{BB} are obtained at the TX by decomposing the joint bit allocation and hybrid precoding optimization problem into a series of sub-problems which are solved using ADMM. Secondly at the RX, the optimal ADC bit resolution matrix Δ_{RX} and hybrid combining matrices W_{RF} and W_{BB} are obtained. The joint optimization problem at the TX and its solution are addressed in the following two subsections, and joint optimization problem and its solution at the RX is addressed in the next section.

A. Problem Formulation at the TX

Focussing on the TX side, we seek the bit resolution matrix Δ_{TX} and the hybrid precoding matrices F_{RF} , F_{BB} that solve the following fractional problem:

$$\begin{split} (\mathcal{P}_{l}): & \max_{\mathbf{F}_{RF}, \boldsymbol{\Delta}_{TX}, \mathbf{F}_{BB}} \frac{R_{TX}(\mathbf{F}_{RF}, \boldsymbol{\Delta}_{TX}, \mathbf{F}_{BB})}{P_{TX}(\mathbf{F}_{RF}, \boldsymbol{\Delta}_{TX}, \mathbf{F}_{BB})}, \\ \text{subject to } \mathbf{F}_{RF} \in \mathcal{F}^{N_{T} \times L_{T}}, \boldsymbol{\Delta}_{TX} \in \mathcal{D}_{TX}^{L_{T} \times L_{T}}, \end{split}$$

where $R_{\text{TX}}(\mathbf{F}_{\text{RF}}, \boldsymbol{\Delta}_{\text{TX}}, \mathbf{F}_{\text{BB}}) = \log_2 \left| \mathbf{I}_{N_{\text{R}}} + \frac{1}{\sigma_{\text{n}}^2 N_{\text{s}}} \mathbf{H} \mathbf{F} \mathbf{F}^H \mathbf{H}^H \right|$, and $P_{\text{TX}}(\mathbf{F}_{\text{RF}}, \boldsymbol{\Delta}_{\text{TX}}, \mathbf{F}_{\text{BB}})$ has been defined in (20). The set \mathcal{D}_{TX} represents the finite states of the quantizer and is defined as,

$$\mathcal{D}_{\text{TX}} = \left\{ \mathbf{\Delta}_{\text{TX}} \in \mathbb{R}^{L_{\text{T}} \times L_{\text{T}}} \middle| m \leq [\mathbf{\Delta}_{\text{TX}}]_{ii} \leq M \; \forall \; i = 1, ..., L_{\text{T}} \right\}.$$

Note that $P_{\text{TX}}(\mathbf{F}_{\text{RF}}, \mathbf{\Delta}_{\text{TX}}, \mathbf{F}_{\text{BB}}) > 0$, as defined in (20), since the power required by all circuit components is always larger than zero, i.e., $P_{\text{CP}} > 0$.

In this work, we adopt the approximation of (\mathcal{P}_1) 's cost function with the following:

$$\begin{split} (\mathcal{P}_{1}'): & \min_{\mathbf{F}_{RF}, \boldsymbol{\Delta}_{TX}, \mathbf{F}_{BB}} & -R(\mathbf{F}_{RF}, \boldsymbol{\Delta}_{TX}, \mathbf{F}_{BB}) + \gamma_{T} P_{TX}(\mathbf{F}_{RF}, \boldsymbol{\Delta}_{TX}, \mathbf{F}_{BB}), \\ & \text{subject to } \mathbf{F}_{RF} \in \mathcal{F}^{N_{T} \times L_{T}}, \boldsymbol{\Delta}_{TX} \in \mathcal{D}_{TX}^{L_{T} \times L_{T}}, \end{split}$$

where $\gamma_T \in \mathbb{R}^+$ is the weighting parameter. Hence, instead of maximizing the ratio of the rate R over power P, we minimize the negative rate -R plus a weighted power term $\gamma_T P_{TX}$.

Moreover, maximization of R (or minimization of -R) can be approximated by finding the minimum Euclidean distance of the hybrid precoder, which is computed by the proposed ADMM approach, to the DBF, i.e., $\|\mathbf{F}_{DBF} - \mathbf{F}_{RF} \boldsymbol{\Delta}_{TX} \mathbf{F}_{BB}\|_F^2$ [6]; as discussed in Section II.C for full-bit resolution sampling case. Therefore, the maximization of the fractional problem (\mathcal{P}_1) can be approximated to finding the solution of the following problem:

$$\begin{split} (\mathcal{P}_2): & \min_{\mathbf{F}_{\text{RF}}, \mathbf{\Delta}_{\text{TX}}, \mathbf{F}_{\text{BB}}} \frac{1}{2} \| \mathbf{F}_{\text{DBF}} - \mathbf{F}_{\text{RF}} \mathbf{\Delta}_{\text{TX}} \mathbf{F}_{\text{BB}} \|_F^2 + \gamma_{\text{T}} P_{\text{TX}}(\mathbf{F}), \\ & \text{subject to } \mathbf{F}_{\text{RF}} \in \mathcal{F}^{N_{\text{T}} \times L_{\text{T}}}, \mathbf{\Delta}_{\text{TX}} \in \mathcal{D}_{\text{TX}}^{L_{\text{T}} \times L_{\text{T}}}, \end{split}$$

where the parameter $\gamma_T \in (0, \gamma_T^{max}] \subset \mathbb{R}^+$ denotes the trade-off between the rate and the power consumption. The main idea to prove equivalence is first to apply the Dinkelbach approach to transform the fractional problem into a affine one [40]. The parameter γ_T also determines how close is the solution of (\mathcal{P}_2) to (\mathcal{P}_1) . In this work γ_T for the TX is selected after an exhaustive search over all possible values in $(0, \gamma_T^{max}] \subset \mathbb{R}^+$ and the value which gives the best result for

 (\mathcal{P}_2) is selected. Problem (\mathcal{P}_2) is non-convex due to the constraints on the structure of matrix \mathbf{F}_{RF} . Also, the cost function and the discrete constraint set of Δ_{TX} are non-convex.

B. Proposed ADMM Solution at the TX

In the following we develop an iterative procedure for solving (\mathcal{P}_2) based on the ADMM approach [33]. This method is a variant of the standard augmented Lagrangian method that uses partial updates (similar to the Gauss-Seidel method for the solution of linear equations) to solve constrained optimization problems. While it is mainly known for its good performance for a number of convex optimization problems, recently it has been successfully applied to nonconvex matrix factorization as well [33], [41], [42]. Motivated by this, in the following ADMM based solutions are developed that are tailored for non-convex matrix factorization problem (\mathcal{P}_2) .

We first transform (\mathcal{P}_2) into a form that can be addressed via ADMM. By using the auxiliary variable \mathbf{Z} , (\mathcal{P}_2) can be written as:

$$(\mathcal{P}_3): \min_{\mathbf{Z}, \mathbf{F}_{\mathsf{RF}}, \boldsymbol{\Delta}_{\mathsf{TX}}, \mathbf{F}_{\mathsf{BB}}} \frac{1}{2} \|\mathbf{F}_{\mathsf{DBF}} - \mathbf{Z}\|_F^2 + \mathbb{1}_{\mathcal{F}^{N_\mathsf{T} \times L_\mathsf{T}}} \{\mathbf{F}_{\mathsf{RF}}\} + \mathbb{1}_{\mathcal{D}_{\mathsf{TX}}^{L_\mathsf{T}} \times L_\mathsf{T}} \{\boldsymbol{\Delta}_{\mathsf{TX}}\} + \gamma_{\mathsf{T}} P_{\mathsf{TX}}(\mathbf{F}),$$
subject to $\mathbf{Z} = \mathbf{F}_{\mathsf{RF}} \boldsymbol{\Delta}_{\mathsf{TX}} \mathbf{F}_{\mathsf{BB}}.$

Problem (\mathcal{P}_3) formulates the A/D hybrid precoder matrix design as a matrix factorization problem. That is, the overall precoder \mathbf{Z} is sought so that it minimizes the Euclidean distance to the optimal, fully digital precoder \mathbf{F}_{DBF} while supporting decomposition into three factors: the analog precoder matrix \mathbf{F}_{RF} , the DAC bit resolution matrix $\mathbf{\Delta}_{TX}$ and the digital precoder matrix \mathbf{F}_{BB} . The augmented Lagrangian function of (\mathcal{P}_3) is given by,

$$\mathcal{L}(\mathbf{Z}, \mathbf{F}_{RF}, \boldsymbol{\Delta}_{TX}, \mathbf{F}_{BB}, \boldsymbol{\Lambda}) = \frac{1}{2} \|\mathbf{F}_{DBF} - \mathbf{Z}\|_{F}^{2} + \mathbb{1}_{\mathcal{F}^{N_{T} \times L_{T}}} \{\mathbf{F}_{RF}\} + \mathbb{1}_{\mathcal{D}_{TX}^{L_{T} \times L_{T}}} \{\boldsymbol{\Delta}_{TX}\}$$
$$+ \frac{\alpha}{2} \|\mathbf{Z} + \boldsymbol{\Lambda}/\alpha - \mathbf{F}_{RF} \boldsymbol{\Delta}_{TX} \mathbf{F}_{BB}\|_{F}^{2} + \gamma_{T} P_{TX}(\mathbf{F}),$$
(25)

where α is a scalar penalty parameter and $\Lambda \in \mathbb{C}^{N_T \times L_T}$ is the Lagrange Multiplier matrix. According to the ADMM approach [33], the solution to (\mathcal{P}_3) is derived by the following iterative steps where n denotes the iteration index:

$$\begin{split} &(\mathcal{P}_{3A}): \mathbf{Z}_{(n)} = \arg\min_{\mathbf{Z}} \mathcal{L}(\mathbf{Z}, \mathbf{F}_{RF(n-1)}, \boldsymbol{\Delta}_{TX(n-1)}, \mathbf{F}_{BB(n-1)}, \boldsymbol{\Lambda}_{(n-1)}), \\ &(\mathcal{P}_{3B}): \mathbf{F}_{RF(n)} = \arg\min_{\mathbf{F}_{RF}} \mathcal{L}(\mathbf{Z}_{(n)}, \mathbf{F}_{RF}, \boldsymbol{\Delta}_{TX(n-1)}, \mathbf{F}_{BB(n-1)}, \boldsymbol{\Lambda}_{(n-1)}), \\ &(\mathcal{P}_{3C}): \boldsymbol{\Delta}_{TX(n)} = \arg\min_{\boldsymbol{\Delta}_{TX}} \mathcal{L}(\mathbf{Z}_{(n)}, \mathbf{F}_{RF(n)}, \boldsymbol{\Delta}_{TX}, \mathbf{F}_{BB(n-1)}, \boldsymbol{\Lambda}_{(n-1)}) + \gamma_{T} P_{TX}(\mathbf{F}), \end{split}$$

$$(\mathcal{P}_{3D}): \mathbf{F}_{BB(n)} = \arg\min_{\mathbf{F}_{BB}} \mathcal{L}(\mathbf{Z}_n, \mathbf{F}_{RF(n)}, \boldsymbol{\Delta}_{TX(n)}, \mathbf{F}_{BB}, \boldsymbol{\Lambda}_{(n-1)}),$$

$$\boldsymbol{\Lambda}_{(n)} = \boldsymbol{\Lambda}_{(n-1)} + \alpha \left(\mathbf{Z}_{(n)} - \mathbf{F}_{RF(n)} \boldsymbol{\Delta}_{TX(n)} \mathbf{F}_{BB(n)} \right).$$
(26)

In order to apply the ADMM iterative procedure, we have to solve the optimization problems (\mathcal{P}_{3A}) - (\mathcal{P}_{3D}) . We may start from problem (\mathcal{P}_{3A}) which can be written as follows:

$$(\mathcal{P}'_{3A}): \ \mathbf{Z}_{(n)} = \arg\min_{\mathbf{Z}} \frac{1}{2} \| (1+\alpha)\mathbf{Z} - \mathbf{F}_{DBF} + \mathbf{\Lambda}_{(n-1)} - \alpha \mathbf{F}_{RF(n-1)} \mathbf{\Delta}_{TX(n-1)} \mathbf{F}_{BB(n-1)} \|_F^2.$$

Problem (\mathcal{P}'_{3A}) can be directly solved by equating the gradient of the augmented Lagrangian (25) w.r.t. **Z** being set to zero. Therefore, we have

$$\mathbf{Z}_{(n)} = \frac{1}{\alpha + 1} \left(\mathbf{F}_{\text{DBF}} - \mathbf{\Lambda}_{(n-1)} + \alpha \mathbf{F}_{\text{RF}(n-1)} \mathbf{\Delta}_{\text{TX}(n-1)} \mathbf{F}_{\text{BB}(n-1)} \right). \tag{27}$$

We may now proceed to solve (\mathcal{P}_{3B}) which can be written in the following simplified form by keeping only the terms of the augmented Lagrangian that are dependent on \mathbf{F}_{RF} :

$$(\mathcal{P}_{3B}'): \ \mathbf{F}_{RF(n)} = \arg\min_{\mathbf{F}_{RF}} \mathbb{1}_{\mathcal{F}^{N_T \times L_T}} \{\mathbf{F}_{RF}\} + \frac{\alpha}{2} \|\mathbf{Z}_{(n)} + \mathbf{\Lambda}_{(n-1)}/\alpha - \mathbf{F}_{RF} \mathbf{\Delta}_{TX(n-1)} \mathbf{F}_{BB(n-1)}\|_F^2.$$

The solution to problem (\mathcal{P}'_{3B}) does not admit a closed form and thus, it is approximated by solving the unconstrained problem and then projecting onto the set $\mathcal{F}^{N_T \times L_T}$, i.e.,

$$\mathbf{F}_{\mathsf{RF}(n)} = \Pi_{\mathcal{F}} \Big\{ \left(\mathbf{\Lambda}_{(n-1)} + \alpha \mathbf{Z}_{(n)} \right) \mathbf{F}_{\mathsf{BB}(n-1)}^{H} \mathbf{\Delta}_{\mathsf{TX}(n-1)}^{H} \left(\alpha \mathbf{\Delta}_{\mathsf{TX}(n-1)} \mathbf{F}_{\mathsf{BB}(n-1)} \mathbf{F}_{\mathsf{BB}(n-1)}^{H} \mathbf{\Delta}_{\mathsf{TX}(n-1)}^{H} \right)^{-1} \Big\},$$
(28)

where $\Pi_{\mathcal{F}}$ projects the solution onto the set \mathcal{F} . This is computed by solving the following optimization problem [43]:

$$(\mathcal{P}_{3B}^{"}): \qquad \min_{\mathbf{A}_{\mathcal{F}}} \|\mathbf{A}_{\mathcal{F}} - \mathbf{A}\|_{F}^{2}, \text{subject to } \mathbf{A}_{\mathcal{F}} \in \mathcal{F},$$

where A is an arbitrary matrix and $A_{\mathcal{F}}$ is its projection onto the set \mathcal{F} . The solution to (\mathcal{P}''_{3B}) is given by the phase of the complex elements of A. Thus, for $A_{\mathcal{F}} = \Pi_{\mathcal{F}}\{A\}$ we have

$$\mathbf{A}_{\mathcal{F}}(x,y) = \begin{cases} 0, & \mathbf{A}(x,y) = 0\\ \frac{\mathbf{A}(x,y)}{|\mathbf{A}(x,y)|}, & \mathbf{A}(x,y) \neq 0 \end{cases}$$
(29)

where $A_{\mathcal{F}}(x,y)$ and A(x,y) are the elements at the xth row-yth column of matrices $A_{\mathcal{F}}$ and A, respectively. While, this is an approximate solution, it turns out that it behaves remarkably well, as verified in the simulation results of Section V. This is due to the interesting property that ADMM is observed to converge even in cases where the alternating minimization steps are

Algorithm 1 Proposed ADMM Solution for the A/D Hybrid Precoder Design

- 1: Initialize: Z, F_{RF} , Δ_{TX} , F_{BB} with random values, Λ with zeros, $\alpha = 1$ and n = 1
- 2: while The termination criteria of (32) are not met or $n \leq N_{\text{max}}$ do
- 3: Update $\mathbf{Z}_{(n)}$ using solution (27),

 $\mathbf{F}_{\mathsf{RF}(n)}$ using solution (28),

 $\Delta_{\text{TX}(n)}$ by solving $(\mathcal{P}''_{3\text{C}})$ using CVX [46],

 $\mathbf{F}_{\mathrm{BB}(n)}$ using solution (30), and

compute $\mathbf{F}_{(n)} = \mathbf{F}_{\mathrm{RF}(n)} \mathbf{\Delta}_{\mathrm{TX}(n)} \mathbf{F}_{\mathrm{BB}(n)}$,

apply the normalization in (31) and

update $\Lambda_{(n)}$ using solution (26).

- 4: $n \leftarrow n + 1$
- 5: end while
- 6: **return** $\mathbf{F}_{RF(N_{max})}$, $\mathbf{\Delta}_{TX(N_{max})}$, $\mathbf{F}_{BB(N_{max})}$

not carried out exactly [33]. There are theoretical results that support this statement [44], [45], though an exact analysis for the case considered here is beyond the scope of this paper.

In a similar manner, (\mathcal{P}_{3C}) may be re-written as,

$$(\mathcal{P}'_{3C}): \boldsymbol{\Delta}_{TX(n)} = \arg\min_{\boldsymbol{\Delta}_{TX}} \mathbb{1}_{\mathcal{D}_{TX}^{L_T \times L_T}} \{\boldsymbol{\Delta}_{TX}\} + \frac{\alpha}{2} \|\mathbf{Z}_{(n)} + \boldsymbol{\Lambda}_{(n-1)}/\alpha - \mathbf{F}_{RF(n)} \boldsymbol{\Delta}_{TX} \mathbf{F}_{BB(n-1)}\|_F^2 + \gamma_T P_{TX}(\mathbf{F}).$$

To solve the above problem, we can write:

$$(\mathcal{P}_{3C}^{''}): \Delta_{TX(n)} = \arg\min_{\Delta_{TX}} \|\mathbf{y}_{c} - \mathbf{\Psi}_{T} \text{vec}(\Delta_{TX})\|_{2}^{2} + \gamma_{T} P_{TX}(\mathbf{F}),$$
subject to $\Delta_{TX} \in \mathcal{D}_{TX}$,

The minimization problem in (\mathcal{P}''_{3C}) consists of $\mathbf{y}_c = \text{vec}(\mathbf{Z}_n + \Lambda_{n-1}/\alpha)$, $\Psi_T = \mathbf{F}_{BB(n-1)} \otimes \mathbf{F}_{RF(n)}$ (\otimes being the Khatri-Rao product) and is solved using CVX [46].

The solution of problem (\mathcal{P}_{3D}) may be written in the following form:

$$(\mathcal{P}'_{3D}): \mathbf{F}_{BB(n)} = \arg\min_{\mathbf{F}_{BB}} \frac{\alpha}{2} \|\mathbf{Z}_{(n)} + \mathbf{\Lambda}_{(n-1)}/\alpha - \mathbf{F}_{RF(n)} \mathbf{\Delta}_{TX(n)} \mathbf{F}_{BB}\|_F^2.$$

It is straightforward to see that the solution for (\mathcal{P}'_{3D}) can be obtained by equating the gradient to zero and solving the resulting equation w.r.t. the matrix variable \mathbf{F}_{BB} , i.e.,

$$\mathbf{F}_{\mathrm{BB}(n)} = \left(\alpha \mathbf{\Delta}_{\mathrm{TX}(n)}^{H} \mathbf{F}_{\mathrm{RF}(n)}^{H} \mathbf{F}_{\mathrm{RF}(n)} \mathbf{\Delta}_{\mathrm{TX}(n)}\right)^{-1} \mathbf{\Delta}_{\mathrm{TX}(n)}^{H} \mathbf{F}_{\mathrm{RF}(n)}^{H} \left(\mathbf{\Lambda}_{(n-1)} + \alpha \mathbf{Z}_{(n)}\right). \tag{30}$$

The following normalization may be applied:

$$\mathbf{F}_{\mathrm{BB}(n)} = \frac{\mathbf{F}_{\mathrm{BB}(n)}}{\mathrm{tr}(\mathbf{F}_{(n)}\mathbf{F}_{(n)}^{H})}.$$
(31)

This is used as otherwise, as stated in [17], it is possible for solutions based on the ADMM approach to violate the constraints of the original problem such as (\mathcal{P}_3) in this paper.

Algorithm 1 provides the complete procedure to obtain the optimal analog precoder matrix \mathbf{F}_{RF} , the optimal bit resolution matrix Δ_{TX} and the optimal baseband (or digital) precoder matrix \mathbf{F}_{BB} . It starts the alternating minimization procedure by initializing the entries of the matrices \mathbf{Z} , \mathbf{F}_{RF} , Δ_{TX} , \mathbf{F}_{BB} with random values and the entries of the Lagrange multiplier matrix $\mathbf{\Lambda}$ with zeros. For iteration index n, $\mathbf{Z}_{(n)}$, $\mathbf{F}_{RF(n)}$, $\Delta_{TX(n)}$ and $\mathbf{F}_{BB(n)}$ are updated using Step 3 which shows the steps to be used to obtain the matrices. A termination criterion related to either the maximum permitted number of iterations (N_{max}) is considered or the ADMM solution meeting the following criteria is considered:

$$\left\| \mathbf{Z}_{(n)} - \mathbf{Z}_{(n-1)} \right\|_{F} \le \epsilon^{z} \, \& \, \left\| \mathbf{Z}_{(n)} - \mathbf{F}_{\mathsf{RF}(n)} \mathbf{\Delta}_{\mathsf{TX}(n)} \mathbf{F}_{\mathsf{BB}(n)} \right\|_{F} \le \epsilon^{p}, \tag{32}$$

where e^z and e^p are the corresponding tolerances. Upon convergence, the number of bits for each DAC is obtained by using (2) and quantizing to the nearest integer value.

Computational complexity analysis of Algorithm 1: When running Algorithm 1, mainly Step 3, while updating $\Delta_{\text{TX}(n)}$ by solving $(\mathcal{P}''_{3\text{C}})$ using CVX, involves multiplication by Ψ_{T} whose dimensions are $L_{\text{T}}N_{\text{T}} \times N_{\text{s}}L_{\text{T}}$. In general, the solution of $(\mathcal{P}''_{3\text{C}})$ can be upper-bounded by $\mathcal{O}((L_{\text{T}}^2N_{\text{T}}N_{\text{s}})^3)$ which can be improved significantly by exploiting the structure of Ψ_{T} .

In the following section, we discuss the joint optimization problem at the RX and the solution to obtain the analog combiner matrix \mathbf{W}_{RF} , the ADC bit resolution matrix $\mathbf{\Delta}_{RX}$ and the digital combiner matrix \mathbf{W}_{BB} .

IV. JOINT ADC BIT ALLOCATION AND A/D HYBRID COMBINING OPTIMIZATION

A. Problem Formulation at the RX

Similar to the joint optimization at the TX, we can express the following fractional problem at the RX:

$$\begin{split} &(\mathcal{P}_4): \ \max_{\mathbf{W}_{RF}, \boldsymbol{\Delta}_{RX}, \mathbf{W}_{BB}} \ \frac{R_{RX}(\mathbf{W}_{RF}, \boldsymbol{\Delta}_{RX}, \mathbf{W}_{BB})}{P_{RX}(\boldsymbol{\Delta}_{RX})} \\ &\text{subject to } \mathbf{W}_{RF} \in \mathcal{W}^{N_R \times L_R}, \boldsymbol{\Delta}_{RX} \in \mathcal{D}_{RX}^{L_R \times L_R}, \end{split}$$

where

$$R_{\text{RX}}(\mathbf{W}_{\text{RF}}, \mathbf{\Delta}_{\text{RX}}, \mathbf{W}_{\text{BB}}) = \log_2 \left| \mathbf{I}_{N_{\text{s}}} + \frac{1}{\sigma_{\text{n}}^2 N_{\text{s}}} \mathbf{W}^H \mathbf{H} \mathbf{F}_{\text{DBF}} \mathbf{F}_{\text{DBF}}^H \mathbf{H}^H \mathbf{W} \right|,$$

and $P_{RX}(\Delta_{RX})$ has been defined in (22). Note, we assume that perfect CSI is available at the TX and the RX, and F_{DBF} is known at the RX. The set \mathcal{D}_{RX} represents the finite states of the ADC quantizer and is defined as,

$$\mathcal{D}_{\text{RX}} = \left\{ \Delta_{\text{RX}} \in \mathbb{R}^{L_{\text{R}} \times L_{\text{R}}} \middle| m \leq [\Delta_{\text{RX}}]_{ii} \leq M \; \forall \; i = 1, ..., L_{\text{R}} \right\}.$$

Following the same steps as we have obtained (\mathcal{P}_2) from (\mathcal{P}_1) , the fractional problem (\mathcal{P}_4) can be approximated by

$$\begin{split} (\mathcal{P}_5): & \min_{\mathbf{W}_{RF}, \boldsymbol{\Delta}_{RX}, \mathbf{W}_{BB}} \frac{1}{2} \| \mathbf{W}_{DBF} - \mathbf{W}_{RF} \boldsymbol{\Delta}_{RX} \mathbf{W}_{BB} \|_F^2 + \gamma_R P_{RX}(\boldsymbol{\Delta}_{RX}), \\ & \text{subject to } \mathbf{W}_{RF} \in \mathcal{W}^{N_R \times L_R}, \boldsymbol{\Delta}_{RX} \in \mathcal{D}_{RX}^{L_R \times L_R}, \end{split}$$

where the parameter $\gamma_R \in (0, \gamma_R^{max}] \subset \mathbb{R}^+$ denotes the trade-off between the rate and the power consumption. Again, we apply the Dinkelbach approach to transform the fractional problem into a affine one [40]. Furthermore, similar to the trade-off parameter at the TX, the parameter γ_R also determines how close is the solution of (\mathcal{P}_5) to (\mathcal{P}_4) . The parameter γ_R is selected after an exhaustive search over all possible values in $(0, \gamma_R^{max}] \subset \mathbb{R}^+$ and the value which gives the best result for (\mathcal{P}_5) is selected. Problem (\mathcal{P}_5) is non-convex due to the constraints on the structure of matrix \mathbf{W}_{RF} and is solved via the ADMM approach.

B. Proposed ADMM Solution at the RX

In the following we develop an iterative procedure for solving (\mathcal{P}_5) based on ADMM [33]. We first transform (\mathcal{P}_5) into an amenable form. By using the auxiliary variable \mathbf{Z} , (\mathcal{P}_5) can be written as:

$$\begin{split} (\mathcal{P}_6): \min_{\mathbf{Z}, \mathbf{W}_{RF}, \boldsymbol{\Delta}_{RX}, \mathbf{W}_{BB}} & \frac{1}{2} \| \mathbf{W}_{DBF} - \mathbf{Z} \|_F^2 + \mathbb{1}_{\mathcal{W}^{N_R \times L_R}} \{ \mathbf{W}_{RF} \} + \mathbb{1}_{\mathcal{D}_{RX}^{L_R \times L_R}} \{ \boldsymbol{\Delta}_{RX} \} + \gamma_R P_{RX}(\boldsymbol{\Delta}_{RX}), \\ & \text{subject to } \mathbf{Z} = \mathbf{W}_{RF} \boldsymbol{\Delta}_{RX} \mathbf{W}_{BB}. \end{split}$$

Problem (\mathcal{P}_6) formulates the A/D hybrid combiner matrix design as a matrix factorization problem. That is, the overall combiner **Z** is sought so that it minimizes the Euclidean distance to the optimal, fully digital combiner \mathbf{W}_{DBF} while supporting the decomposition into the analog

Algorithm 2 Proposed ADMM Solution for the A/D Hybrid Combiner Design

- 1: Initialize: Z, W_{RF}, Δ_{RX} , W_{BB} with random values, Λ with zeros, $\alpha = 1$ and n = 1
- 2: while $n \leq N_{\text{max}}$ do
- 3: Update $\mathbf{Z}_{(n)}$ using solution (35),

 $\mathbf{W}_{\mathrm{RF}(n)}$ using solution (36),

 $\Delta_{RX(n)}$ by solving (\mathcal{P}_{6C}) using CVX [46],

 $\mathbf{W}_{\mathrm{BB}(n)}$ using solution (37), and

compute $\mathbf{W}_{(n)} = \mathbf{W}_{\mathrm{RF}(n)} \mathbf{\Delta}_{\mathrm{RX}(n)} \mathbf{W}_{\mathrm{BB}(n)}$,

apply the normalization in (38) and

update $\Lambda_{(n)}$ using solution (34).

- 4: $n \leftarrow n + 1$
- 5: end while
- 6: **return** $\mathbf{W}_{RF(N_{max})}$, $\boldsymbol{\Delta}_{RX(N_{max})}$, $\mathbf{W}_{BB(N_{max})}$

combiner matrix W_{RF} , the quantization error matrix Δ_{RX} and the digital combiner matrix W_{BB} . The augmented Lagrangian function of (\mathcal{P}_6) is given by,

$$\mathcal{L}(\mathbf{Z}, \mathbf{W}_{RF}, \boldsymbol{\Delta}_{RX}, \mathbf{W}_{BB}, \boldsymbol{\Lambda}) = \frac{1}{2} \|\mathbf{W}_{DBF} - \mathbf{Z}\|_{F}^{2} + \mathbb{1}_{\mathcal{W}^{N_{R} \times L_{R}}} \{\mathbf{W}_{RF}\} + \mathbb{1}_{\mathcal{D}_{RX}^{L_{R} \times L_{R}}} \{\boldsymbol{\Delta}_{RX}\}$$
$$+ \frac{\alpha}{2} \|\mathbf{Z} + \boldsymbol{\Lambda}/\alpha - \mathbf{W}_{RF} \boldsymbol{\Delta}_{RX} \mathbf{W}_{BB}\|_{F}^{2} + \gamma_{R} P_{RX}(\boldsymbol{\Delta}_{RX}), \tag{33}$$

where α is a scalar penalty parameter and $\Lambda \in \mathbb{C}^{N_R \times L_R}$ is the Lagrange Multiplier matrix. According to the ADMM approach [33], the solution to (\mathcal{P}_6) is derived by the following iterative steps:

$$(\mathcal{P}_{6A}): \mathbf{Z}_{(n)} = \arg\min_{\mathbf{Z}} \frac{1}{2} \| (1+\alpha)\mathbf{Z} - \mathbf{W}_{DBF} + \mathbf{\Lambda}_{(n-1)} - \alpha \mathbf{W}_{RF(n-1)} \mathbf{\Delta}_{RX(n-1)} \mathbf{W}_{BB(n-1)} \|_{F}^{2},$$

$$(\mathcal{P}_{6B}): \mathbf{W}_{RF(n)} = \arg\min_{\mathbf{W}_{RF}} \mathbb{1}_{\mathcal{W}^{N_{R} \times L_{R}}} \{\mathbf{W}_{RF}\} + \frac{\alpha}{2} \| \mathbf{Z}_{(n)} + \mathbf{\Lambda}_{(n-1)} / \alpha - \mathbf{W}_{RF} \mathbf{\Delta}_{RX(n-1)} \mathbf{W}_{BB(n-1)} \|_{F}^{2},$$

$$(\mathcal{P}_{6C}): \mathbf{\Delta}_{RX(n)} = \arg\min_{\mathbf{\Delta}_{RX}} \| \mathbf{y}_{c} - \mathbf{\Psi}_{R} \text{vec}(\mathbf{\Delta}_{RX}) \|_{2}^{2} + \gamma_{R} P_{RX}(\mathbf{\Delta}_{RX}) \text{ subject to } \mathbf{\Delta}_{RX} \in \mathcal{D}_{RX},$$

$$(\mathcal{P}_{6D}): \mathbf{W}_{BB(n)} = \arg\min_{\mathbf{W}_{BB}} \frac{\alpha}{2} \| \mathbf{Z}_{(n)} + \mathbf{\Lambda}_{(n-1)} / \alpha - \mathbf{W}_{RF(n)} \mathbf{\Delta}_{RX(n)} \mathbf{W}_{BB} \|_{F}^{2},$$

$$\mathbf{\Lambda}_{(n)} = \mathbf{\Lambda}_{(n-1)} + \alpha \left(\mathbf{Z}_{(n)} - \mathbf{W}_{RF(n)} \mathbf{\Delta}_{RX(n)} \mathbf{W}_{BB(n)} \right),$$

$$(34)$$

where n denotes the iteration index, $\mathbf{y}_c = \text{vec}(\mathbf{Z}_{(n)} + \mathbf{\Lambda}_{(n-1)}/\alpha)$ and $\mathbf{\Psi}_R = \mathbf{W}_{BB(n-1)} \otimes \mathbf{W}_{RF(n)}$ (\otimes is the Khatri-Rao product).

We solve the optimization problems (\mathcal{P}_{6A}) - (\mathcal{P}_{6D}) in a similar way to the derivations in Section III for the TX. The solution for $\mathbf{Z}_{(n)}$ is:

$$\mathbf{Z}_{(n)} = \frac{1}{\alpha + 1} \left(\mathbf{W}_{\text{DBF}} - \mathbf{\Lambda}_{(n-1)} + \alpha \mathbf{W}_{\text{RF}(n-1)} \mathbf{\Delta}_{\text{RX}(n-1)} \mathbf{W}_{\text{BB}(n-1)} \right). \tag{35}$$

The equation for $W_{RF(n)}$ is as follows:

$$\mathbf{W}_{RF(n)} = \Pi_{\mathcal{W}} \left\{ \left(\mathbf{\Lambda}_{(n-1)} + \alpha \mathbf{Z}_{(n)} \right) \mathbf{W}_{BB}_{(n-1)}^{H} \mathbf{\Delta}_{RX(n-1)}^{H} \right.$$

$$\left. \left\{ \alpha \mathbf{\Delta}_{RX(n-1)} \mathbf{W}_{BB(n-1)} \mathbf{W}_{BB(n-1)}^{H} \mathbf{\Delta}_{RX(n-1)}^{H} \right\}^{-1} \right\}.$$
(36)

The solution to $\Delta_{RX(n)}$ is obtained by solving (\mathcal{P}_{6C}) using CVX [46]. The matrix $\mathbf{W}_{BB(n)}$ is obtained as follows:

$$\mathbf{W}_{\mathrm{BB}(n)} = \left\{ \alpha \mathbf{\Delta}_{\mathrm{RX}(n)}^{H} \mathbf{W}_{\mathrm{RF}(n)}^{H} \mathbf{W}_{\mathrm{RF}(n)} \mathbf{\Delta}_{\mathrm{RX}(n)} \right\}^{-1} \mathbf{\Delta}_{\mathrm{RX}(n)}^{H} \mathbf{W}_{\mathrm{RF}(n)} \left(\mathbf{\Lambda}_{(n-1)} + \alpha \mathbf{Z}_{(n)} \right). \tag{37}$$

Again, the following normalization may be applied:

$$\mathbf{W}_{\mathrm{BB}(n)} = \frac{\mathbf{W}_{\mathrm{BB}(n)}}{\mathrm{tr}(\mathbf{W}_{(n)}\mathbf{W}_{(n)}^{H})}.$$
(38)

Algorithm 2 provides the complete procedure to obtain W_{RF} , Δ_{RX} and W_{BB} . It starts by initializing the entries of the matrices Z, W_{RF} , Δ_{RX} , W_{BB} with random values and the entries of the Lagrange multiplier matrix Λ with zeros. For iteration index n, $Z_{(n)}$, $W_{RF(n)}$, $\Delta_{RX(n)}$, $W_{BB(n)}$ are updated at each iteration step by using the solution in (35), (36), solving (\mathcal{P}_{6C}) using CVX, (37) and (34), respectively. The operator $\Pi_{\mathcal{W}}$ projects the solution onto the set \mathcal{W} . This procedure is identical to problem $(\mathcal{P}_{3B}^{"})$ in Section III, except that the set \mathcal{W} replaces \mathcal{F} . A termination criterion is defined using a maximum number of iterations (N_{max}) or a fidelity criterion similar to (32). Upon convergence, the number of bits for each ADC is obtained by using (3) and quantizing to the nearest integer value.

Computational complexity analysis of Algorithm 2: Similar to Algorithm 1 for the TX, the complexity of the solution of (\mathcal{P}_{6C}) can be upper-bounded by $\mathcal{O}((L_R^2N_RN_s)^3)$ which can be improved significantly by exploiting the structure of Ψ_R .

Once the optimal DAC and ADC bit resolution matrices, i.e., Δ_{TX} and Δ_{RX} , and optimal hybrid precoding and combining matrices, i.e., F_{RF} , F_{BB} and W_{RF} , W_{BB} , are obtained then they can be plugged into (18) and (19) to obtain the maximum EE in (17). In the next section, we discuss the simulation results based on the proposed solution at the TX and the RX, and comparison with existing benchmark techniques.

V. SIMULATION RESULTS

In this section, we evaluate the performance of the proposed ADMM solution using computer simulation results. All the results have been averaged over 1000 Monte-Carlo realizations. For comparison with the proposed ADMM solution, we consider several existing benchmark techniques as follows:

- 1) Digital beamforming with 8-bit resolution: We consider the conventional fully digital beamforming architecture, where the number of RF chains at the TX/RX is equal to the number of TX/RX antennas, i.e., $L_{\rm T}=N_{\rm T}$ and $L_{\rm R}=N_{\rm R}$. In terms of the resolution sampling, we consider full-bit resolution, i.e., M=8-bit, which represents the best case from the achievable SE perspective.
- 2) A/D Hybrid beamforming with 1-bit and 8-bit resolutions: We also consider a A/D hybrid beamforming architecture with $L_{\rm T} < N_{\rm T}$ and $L_{\rm R} < N_{\rm R}$, for two cases of DAC/ADC bit resolution: a) 1-bit resolution which usually shows reasonable EE performance, and b) 8-bit resolution which usually shows high SE results.
- 3) Brute force with A/D hybrid beamforming: We also implement an exhaustive search approach as an upper bound for EE maximization called brute force (BF), based on [16]. Firstly the EE problem is split into TX and RX optimization problems similar to those for the proposed ADMM approach. Then it makes a search over all the possible DAC and ADC bit resolutions in the range of [m, M] associated with the each RF chain from 1 to L_T and 1 to L_R at the TX and the RX, respectively. It then finds the best EE out of all the possible cases and chooses the corresponding optimal resolution for each DAC and ADC. This method provides the best possible EE performance and serves as upper bound for EE maximization by the ADMM approach.

Complexity comparison with the BF approach: The proposed ADMM solution has lower complexity than the upper bound BF approach because the BF technique involves a search over all the possible DAC/ADC bit resolutions while the proposed ADMM solution directly optimizes the number of bits at each DAC/ADC. We constrain the number of RF chains $L_T = L_R = 5$ for the BF approach due to the high complexity order which is $\mathcal{O}(M^{L_T})$ and $\mathcal{O}(M^{L_R})$ at the TX and the RX, respectively.

System setup: We set the following parameters, unless specified otherwise, to obtain the desired results: $N_{\rm T}=32$, $L_{\rm T}=5$, $N_{\rm s}=L_{\rm T}$, $L_{\rm R}=L_{\rm T}$, $N_{\rm R}=5$, $N_{\rm cl}=2$, $N_{\rm ray}=3$, $N_{\rm max}=20$, m=1, M=8, $\gamma_{\rm T}^{max}=0.1$, $\gamma_{\rm R}^{max}=1$, $\alpha=1$ and $\sigma_{\alpha,i}^2=1$. The azimuth angles of departure and arrival are computed with uniformly distributed mean angles, and each cluster follows a Laplacian

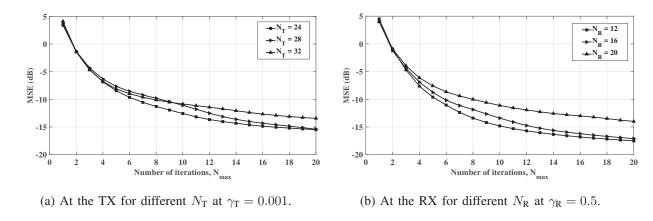


Fig. 2: Convergence of the proposed ADMM solution at the TX and the RX.

distribution about the mean angle. The antenna elements in the ULA are spaced by distance $d=\lambda/2$. The signal-to-noise ratio (SNR) is given by the inverse of the noise variance, i.e., $1/\sigma_{\rm n}^2$. The transmit vector ${\bf x}$ is composed of the normalized i.i.d. Gaussian symbols. The values used for the power terms [47] in the power model equations in (20) and (22) are $P_{\rm DAC}=P_{\rm ADC}=100$ mW, $P_{\rm CT}=P_{\rm CR}=10$ W, $P_{\rm T}=P_{\rm R}=100$ mW and $P_{\rm PT}=P_{\rm PR}=10$ mW.

Convergence of the proposed ADMM solution: Figs. 2 (a) and 2 (b) show the convergence of the ADMM solution at the TX and the RX as proposed in Algorithm 1 and Algorithm 2, respectively, to obtain the optimal bit resolution at each DAC/ADC and the corresponding optimal precoder/combiner matrices. It can be observed from Fig. 2 (a) that the proposed solution converges rapidly within 16 iterations and the normalized mean square error (NMSE) at the TX, $\|\mathbf{F}_{\mathrm{DBF}} - \mathbf{F}_{\mathrm{RF}(N_{\mathrm{max}})} \Delta_{\mathrm{TX}(N_{\mathrm{max}})} \mathbf{F}_{\mathrm{BB}(N_{\mathrm{max}})}\|_F^2 / \|\mathbf{F}_{\mathrm{DBF}}\|_F^2$, goes as low as -15 dB. Similarly, in Fig. 2 (b), the proposed solution again converges rapidly and the NMSE at the RX, $\|\mathbf{W}_{\mathrm{DBF}} - \mathbf{W}_{\mathrm{RF}(N_{\mathrm{max}})} \Delta_{\mathrm{RX}(N_{\mathrm{max}})} \mathbf{W}_{\mathrm{BB}(N_{\mathrm{max}})}\|_F^2 / \|\mathbf{W}_{\mathrm{DBF}}\|_F^2$, goes as low as -17 dB. A lower number of TX/RX antennas shows lower NMSE for a given number of iterations as expected, since fewer parameters are required to be estimated.

Fig. 3 shows the performance of the proposed ADMM solution compared with existing benchmark techniques w.r.t. SNR at $\gamma_T = 0.001$ and $\gamma_R = 0.5$. The proposed ADMM solution achieves high EE which is computed by (17) after obtaining the optimal DAC and ADC bit resolution matrices, i.e., Δ_{TX} and Δ_{RX} , and optimal hybrid precoding and combining matrices, i.e., F_{RF} , F_{BB} and W_{RF} , W_{BB} . The results are plugged into (18) and (19) to evaluate rate and power respectively. The EE for the proposed solution has similar performance to the BF

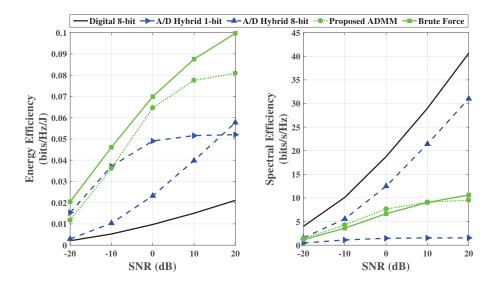


Fig. 3: EE and SE performance w.r.t. SNR at $\gamma_{\rm T}=0.001$ and $\gamma_{\rm R}=0.5$.

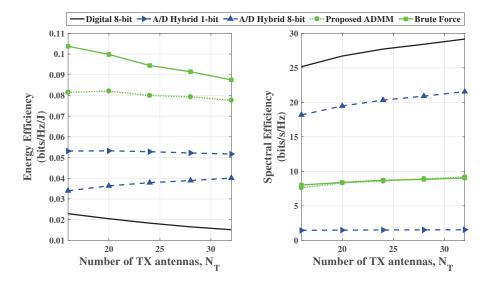


Fig. 4: EE and SE performance w.r.t. $N_{\rm T}$ at SNR = 10 dB, $\gamma_{\rm T}=0.001$ and $\gamma_{\rm R}=0.5$.

approach and is better than the hybrid 1-bit, the hybrid 8-bit and the digital full-bit baselines. For example, at SNR = 10 dB, the proposed ADMM solution outperforms the hybrid 1-bit, the hybrid 8-bit and the digital full-bit baselines by about 0.03 bits/Hz/J, 0.04 bits/Hz/J and 0.065 bits/Hz/J, respectively.

The proposed solution also exhibits better SE, which is the rate in (18) after obtaining the optimal DAC and ADC bit resolution matrices, and optimal hybrid precoding and combining matrices, than the hybrid 1-bit and has similar performance to the BF approach for high and

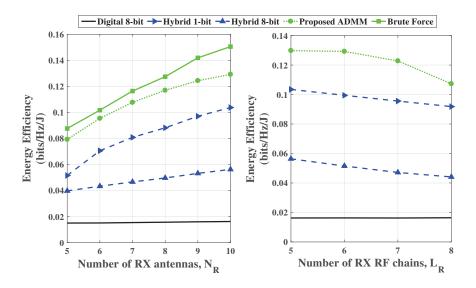


Fig. 5: EE performance w.r.t. $N_{\rm R}$ and $L_{\rm R}$ at SNR = 10 dB, $\gamma_{\rm T}=0.001$ and $\gamma_{\rm R}=0.5$.

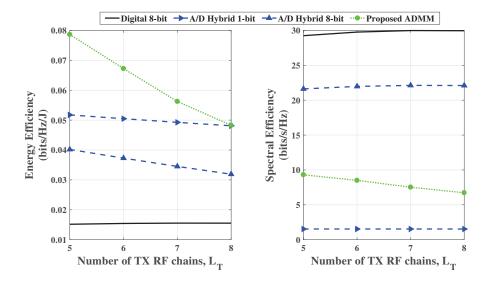


Fig. 6: EE and SE performance w.r.t. L_T at SNR = 10 dB, $\gamma_T = 0.001$ and $\gamma_R = 0.5$.

low SNR regions and hybrid 8-bit baseline for low SNR region. Note that the proposed ADMM solution enables the selection of different resolutions for different DACs/ADCs and thus, it offers a better trade-off for EE versus SE than existing approaches which are based on a fixed DAC/ADC bit resolution.

Fig. 4 shows the EE (from (17)) and SE (from (18)) performance results w.r.t. the number of TX antennas $N_{\rm T}$ at 10 dB SNR, $\gamma_{\rm T}=0.001$ and $\gamma_{\rm R}=0.5$. The proposed ADMM solution again achieves high EE and performs similar to the BF approach and better than the hybrid 1-bit, the

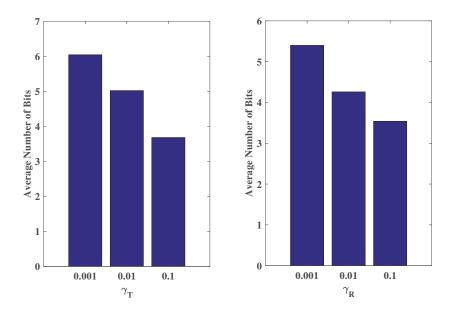


Fig. 7: Average number of bits for proposed ADMM w.r.t. γ_T and γ_R at the TX and the RX, respectively, at SNR = 10 dB.

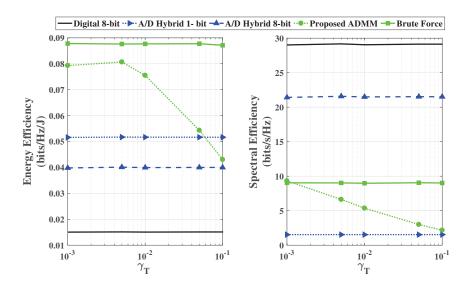


Fig. 8: EE and SE performance w.r.t. $\gamma_{\rm T}$ at SNR = 10 dB.

hybrid 8-bit and the digital full-bit baselines. For example, at $N_{\rm T}=20$, the proposed ADMM solution outperforms hybrid 1-bit, the hybrid 8-bit and the digital full-bit baselines by about 0.03 bits/Hz/J, 0.045 bits/Hz/J and 0.06 bits/Hz/J, respectively. The proposed ADMM solution also exhibits SE performance similar to the BF approach and better than the hybrid 1-bit baseline.

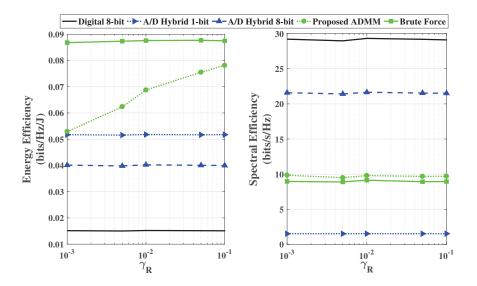


Fig. 9: EE and SE performance w.r.t. γ_R at SNR = 10 dB.

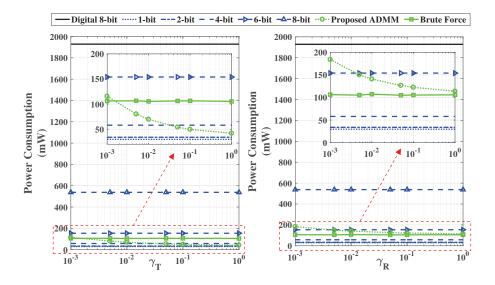


Fig. 10: Power consumption w.r.t. γ_T and γ_R at the TX and RX, respectively, at SNR = 10 dB.

Fig. 5 shows the EE performance results w.r.t. the number of RX antennas $N_{\rm R}$ and the number of RX RF chains $L_{\rm R}$, respectively, at 10 dB SNR, $\gamma_{\rm T}=0.001$ and $\gamma_{\rm R}=0.5$. The proposed ADMM solution again achieves high EE which decreases with increase in the number of RX RF chains, and performs similar to the BF approach (for versus $N_{\rm R}$) and better than the hybrid 1-bit, the hybrid 8-bit and the digital full-bit baselines. For example, at $N_{\rm R}=7$, the proposed ADMM solution outperforms hybrid 1-bit, the hybrid 8-bit and the digital full-bit baselines by about 0.03 bits/Hz/J, 0.06 bits/Hz/J and 0.09 bits/Hz/J, respectively. Also, for example, at

 $L_{\rm R}=6$, the proposed ADMM solution outperforms hybrid 1-bit, the hybrid 8-bit and the digital full-bit baselines by about 0.025 bits/Hz/J, 0.08 bits/Hz/J and 0.115 bits/Hz/J, respectively. Note that, due to the high complexity of the BF approach, we do not plot results for this approach w.r.t. $L_{\rm T}$ and $L_{\rm R}$.

Fig. 6 shows the EE and SE performance results w.r.t. the number of TX RF chains $L_{\rm T}$ at 10 dB SNR, $\gamma_{\rm T}=0.001$ and $\gamma_{\rm R}=0.5$. The proposed ADMM solution achieves high EE, though this decreases with increase in the number of TX RF chains ADMM achieves better EE performance than the hybrid 1-bit, the hybrid 8-bit and the digital full-bit resolution baselines. Also, the proposed ADMM solution exhibits SE performance better than the hybrid 1-bit baseline.

Furthermore, we investigate the performance over the trade-off parameters γ_T and γ_R introduced in (\mathcal{P}_2) and (\mathcal{P}_5) , respectively. Fig. 7 shows the bar plot of the average of the optimal number of bits selected by the proposed ADMM solution for each DAC versus γ_T and for each ADC versus γ_R . It can be observed that the average optimal number decreases with the increase in γ_T and γ_R , for example, the average number of DAC bits is around 6 for $\gamma_T = 0.001$, 5 for $\gamma_T = 0.01$ and 4 for $\gamma_T = 0.1$. Similarly, at the RX, the average number of ADC bits is about 5 for $\gamma_R = 0.001$, 4 for $\gamma_R = 0.01$ and 3 for $\gamma_R = 0.1$. This is because increasing γ_T or γ_R gives more weight to the power consumption.

Figs. 8 and 9 show the EE and SE plots for several solutions w.r.t. γ_T and γ_R at the TX and the RX, respectively. It can be observed that the proposed solution achieves higher EE performance than the fixed bit allocation solutions such as the digital full-bit, the hybrid 1-bit and the hybrid 8-bit baselines and achieves comparable EE and SE results to the BF approach. These curves also show that adjusting γ_T and γ_R values allow the system to vary the energy-rate trade-off. Note that the TX also accounts for the extra power term, i.e., $\text{tr}(\mathbf{F}\mathbf{F}^H)$ as shown in (20) which means that the selected γ_T parameter at the TX is lower than the selected γ_R parameter at the RX. Fig. 10 shows that the power consumption in the proposed case is low and decreases with the increase in the trade-off parameter γ_T and γ_R values unlike digital 8-bit, fixed bit resolution hybrid baselines and the BF approach.

VI. CONCLUSION

This paper proposes an energy efficient mmWave A/D hybrid MIMO system which can vary dynamically the DAC and ADC bit resolutions at the TX and the RX, respectively. This method uses the decomposition of the A/D hybrid precoder/combiner matrix into three parts

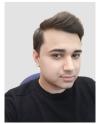
representing the analog precoder/combiner matrix, the DAC/ADC bit resolution matrix and the digital precoder/combiner matrix. These three matrices are optimized by a novel ADMM solution which outperforms the EE of the digital full-bit, the hybrid 1-bit beamforming and the hybrid 8-bit beamforming baselines, for example, by 3%, 4% and 6.5%, respectively, for a typical value of 10 dB SNR. There is an energy-rate trade-off with the BF approach which yields the upper bound for EE maximization and the proposed ADMM solution exhibits lower computational complexity. Moreover, the proposed ADMM solution enables the selection of the optimal resolution for each DAC/ADC and thus, it offers better trade-off for data rate versus EE than existing approaches that are based on a fixed DAC/ADC bit resolution.

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